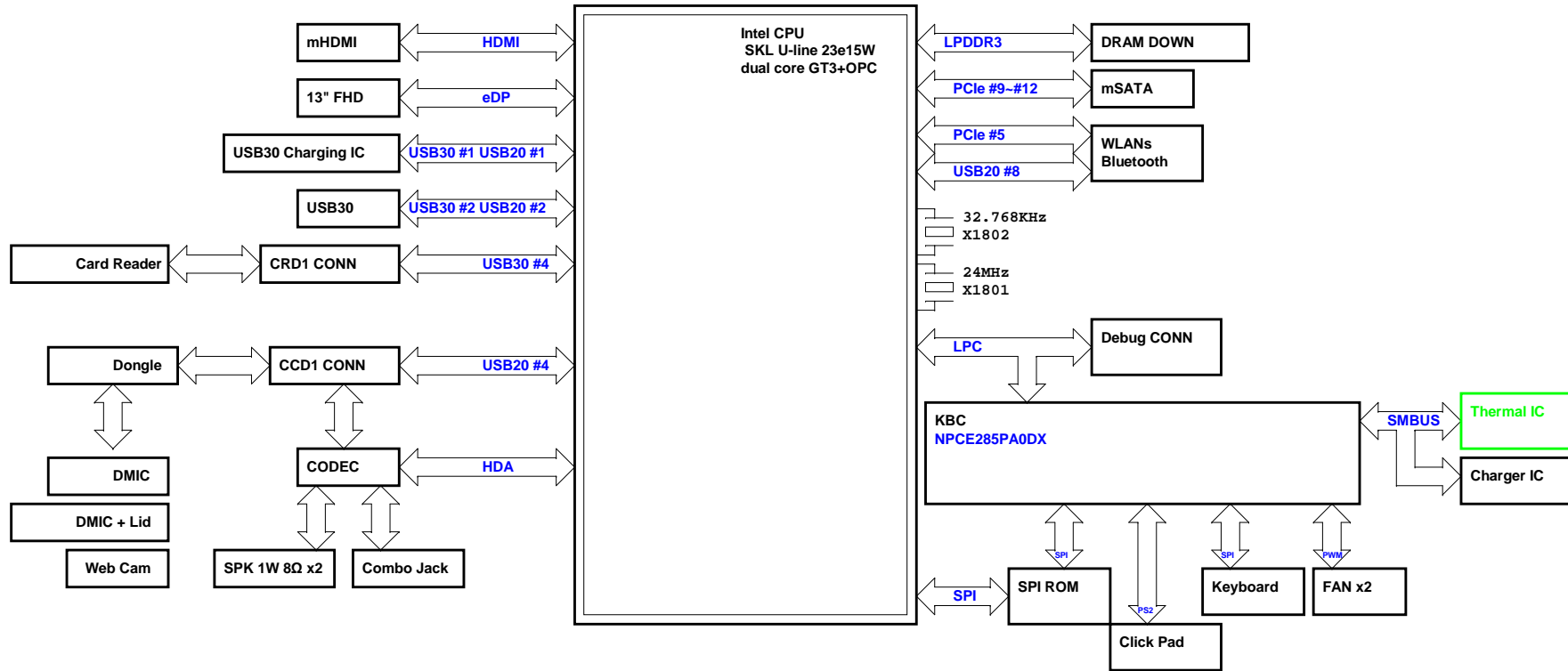


## Block Diagram



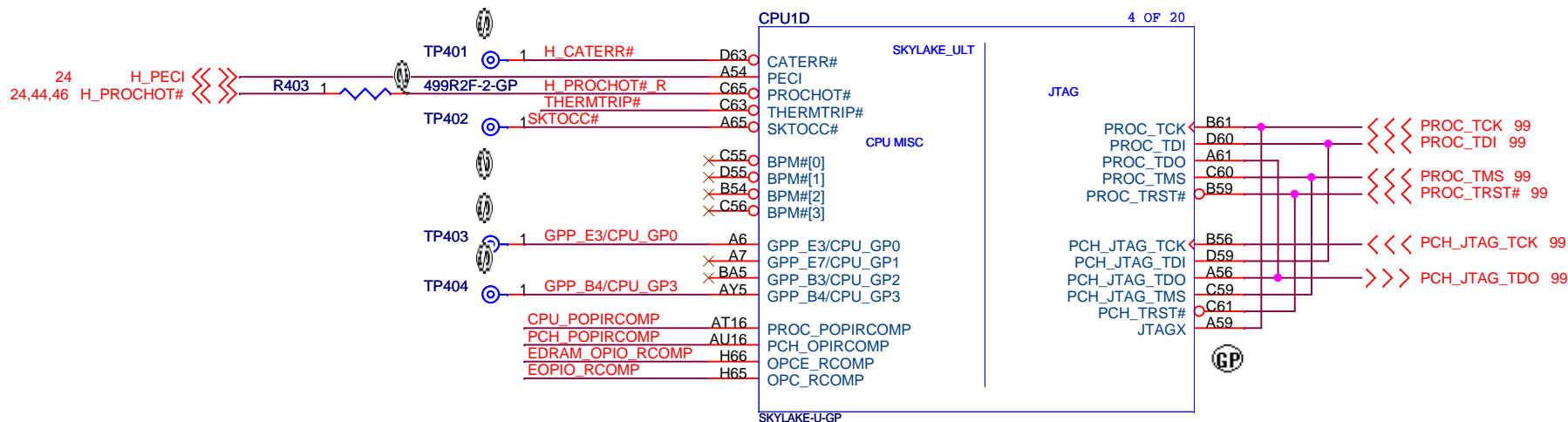
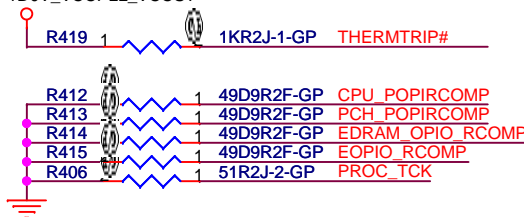
<b>Charger IC</b>	
Input	Output
19V_AD+	19V_DCBATOUT
12V_BT+	
<b>System DC/DC IC</b>	
Input	Output
19V_DCBATOUT	3D3V_AUX_S5
	3D3V_S5
	5V_AUX_S5
	5V_S5
<b>CPU PWR IC</b>	
Input	Output
19V_DCBATOUT	1V_CPU_CORE
	1V_VCCGT
	1V_VCCSA
<b>LPDDR3 PWR IC</b>	
Input	Output
19V_DCBATOUT	1D2V_S3
	0D6V_S0
<b>CPU PWR IC</b>	
Input	Output
19V_DCBATOUT	1D0V_S5
<b>CPU PWR IC</b>	
Input	Output
3D3V_S5	1D8V_S5
<b>Load Switch</b>	
Input	Output
5V_S5	5V_S5
3D3V_S5	3D3V_S5
<b>Load Switch</b>	
Input	Output
1D0V_S5	1D0V_VCCPLL_VCCST
<b>Load Switch</b>	
Input	Output
1D0V_S5	1D8V_VCCIO
<b>Load Switch</b>	
Input	Output
1D0V_S5	1D0V_VCCOPC
	1D0V_VCCEOPIO
<b>Load Switch</b>	
Input	Output
3D3V_S5	1D8V_MEM
<b>Load Switch</b>	
Input	Output
3D3V_S5	1D8V_S0

SKL U-line 23e 15W

1D0V\_VCCSTG



1D0V\_VCCPLL\_VCCST



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緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title

CPU (JTAG/CPU SIDE BAND)

Size  
A4

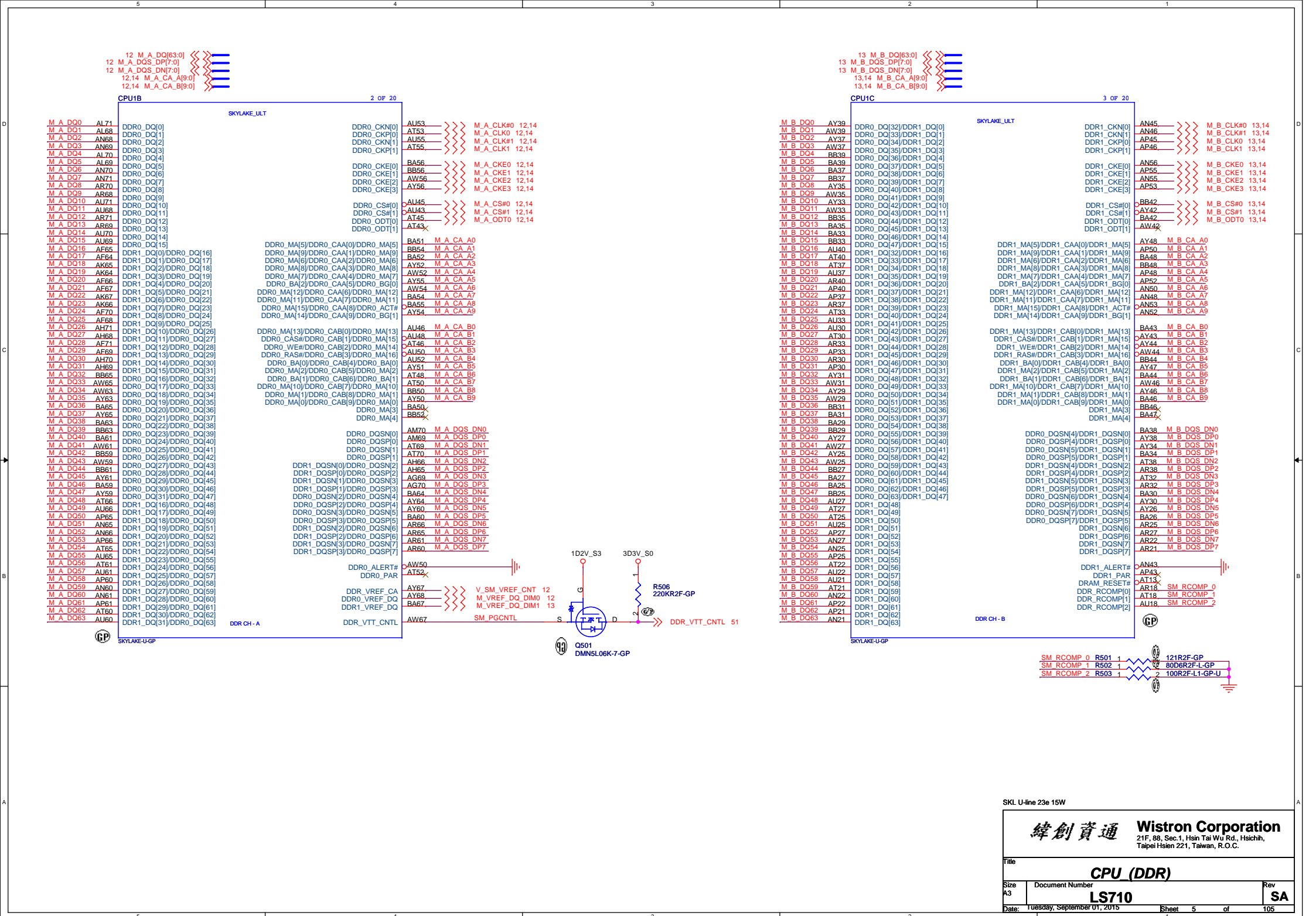
Document Number

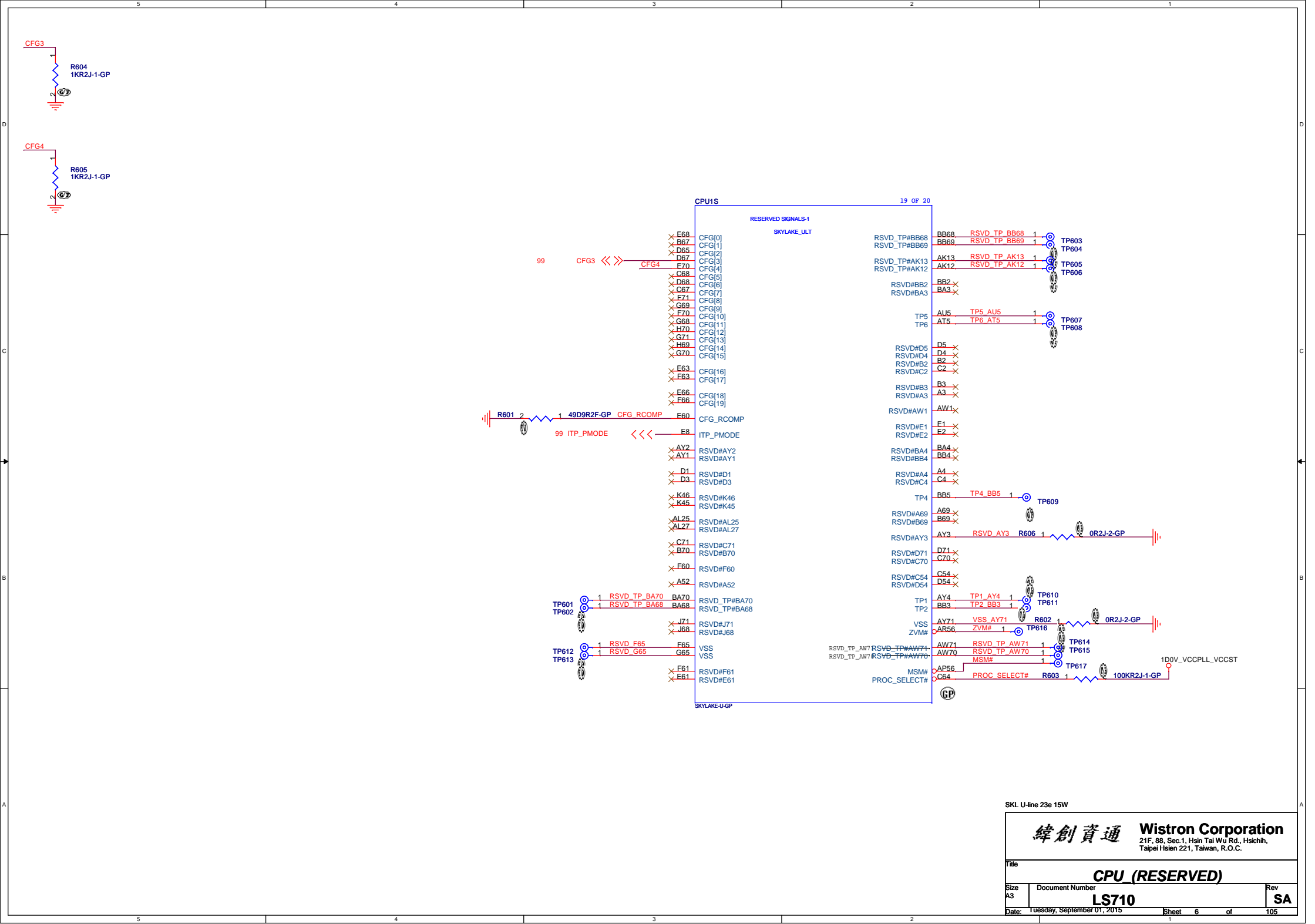
LS710

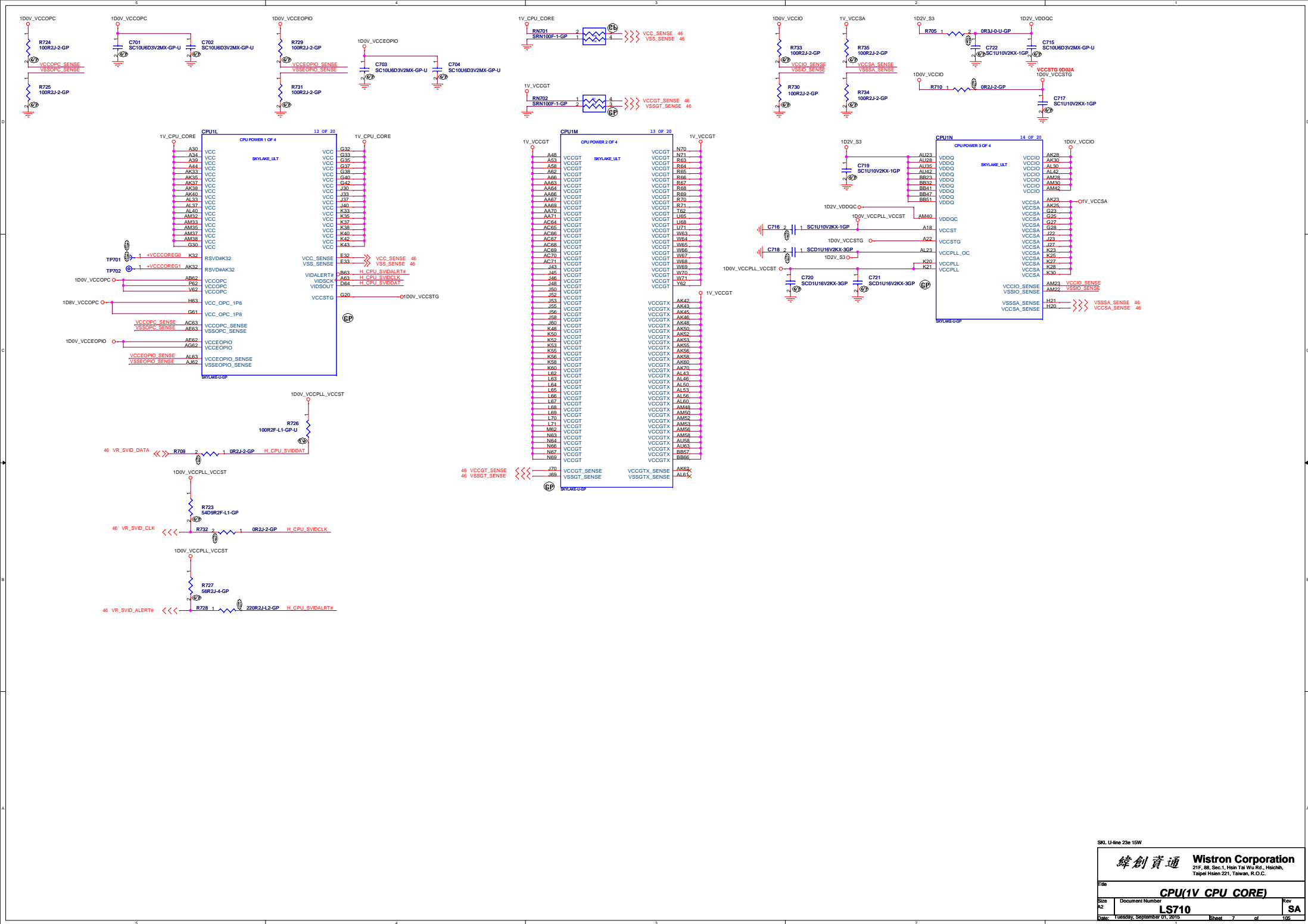
Rev  
SA

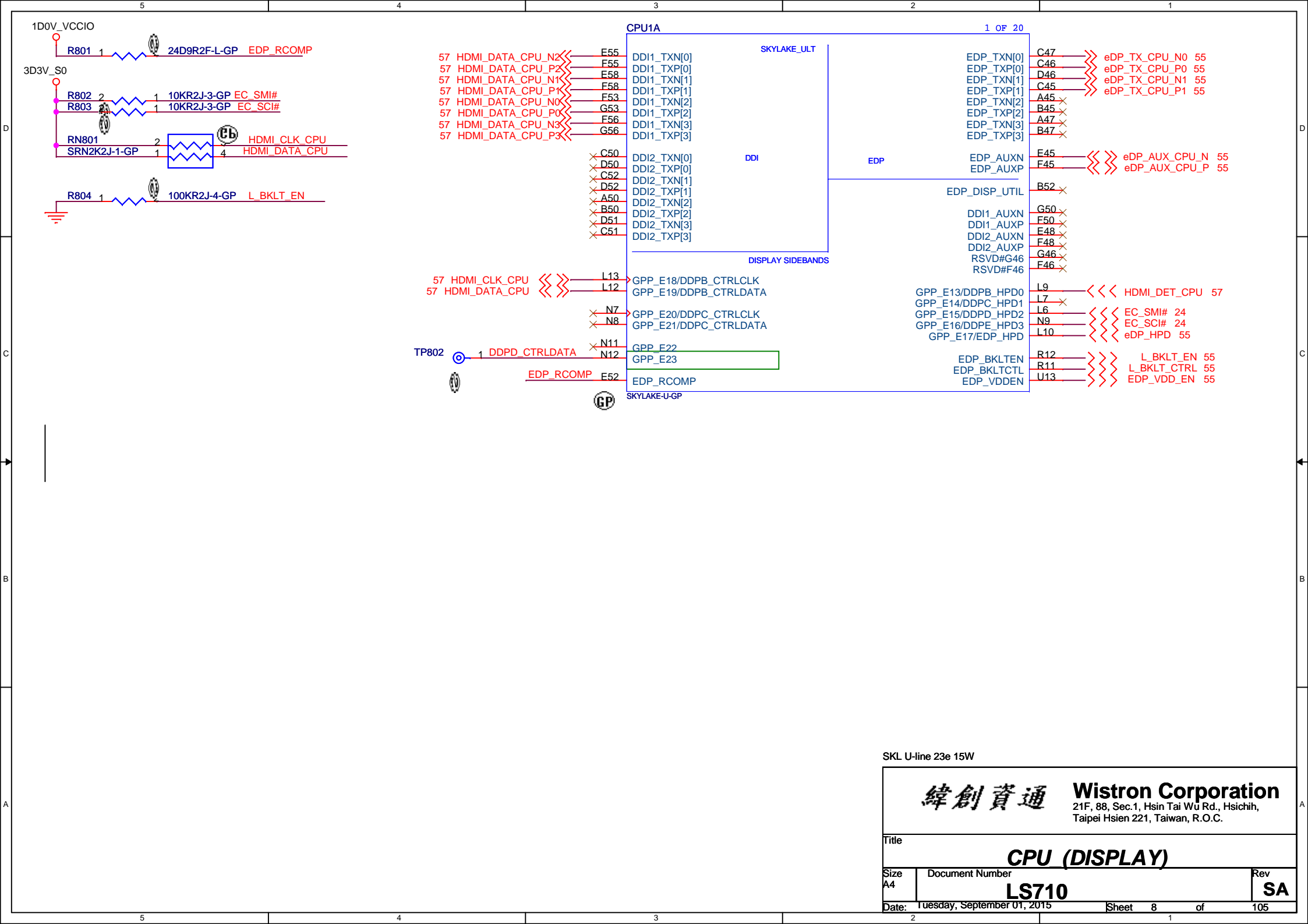
Date: Tuesday, September 01, 2015

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緯創資通

Wistron Corporation

21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.

Title

CPU (DISPLAY)

Size

A4

Document Number

LS710

Rev

SA

Date:

Tuesday, September 01, 2015

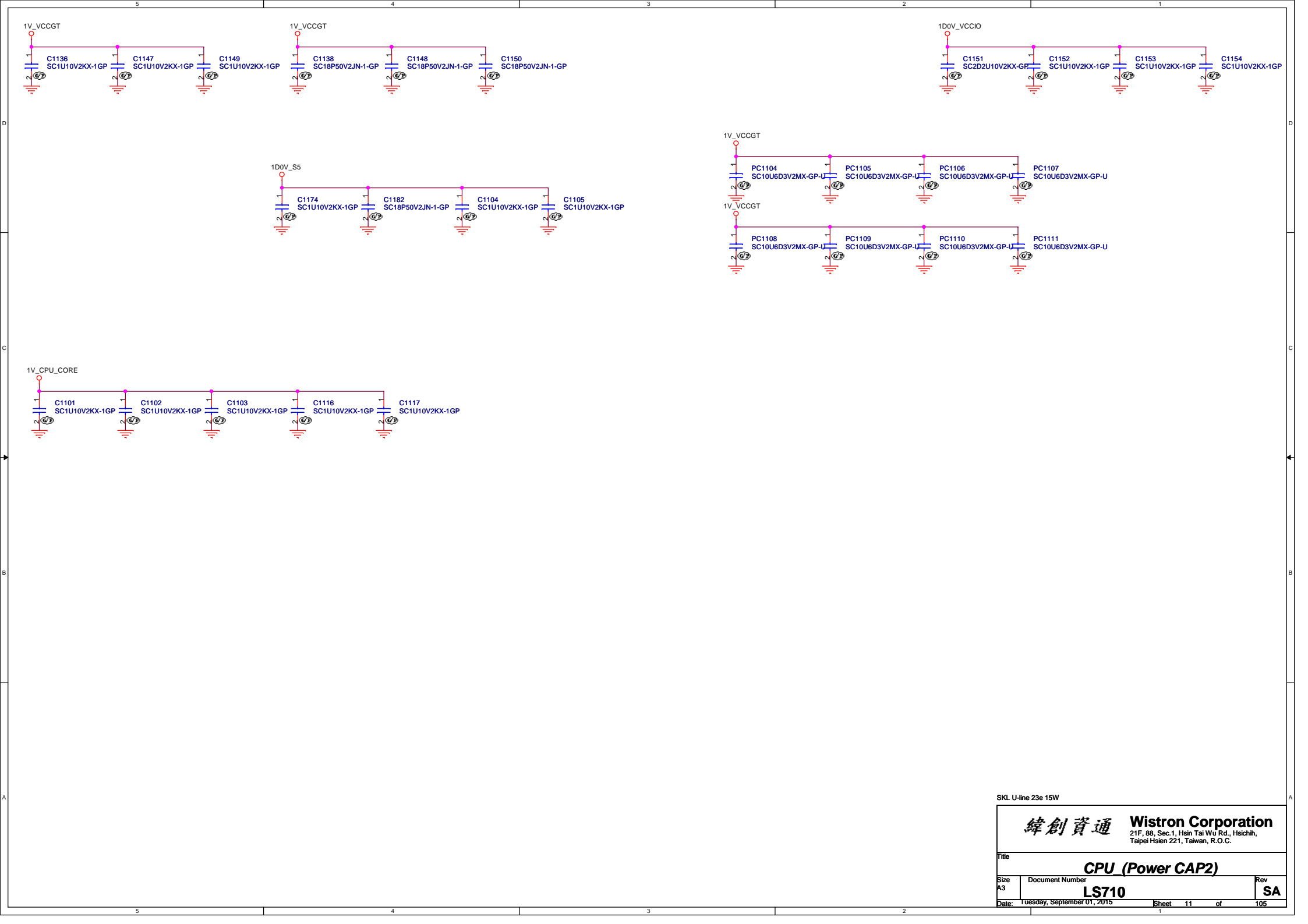
Sheet

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of

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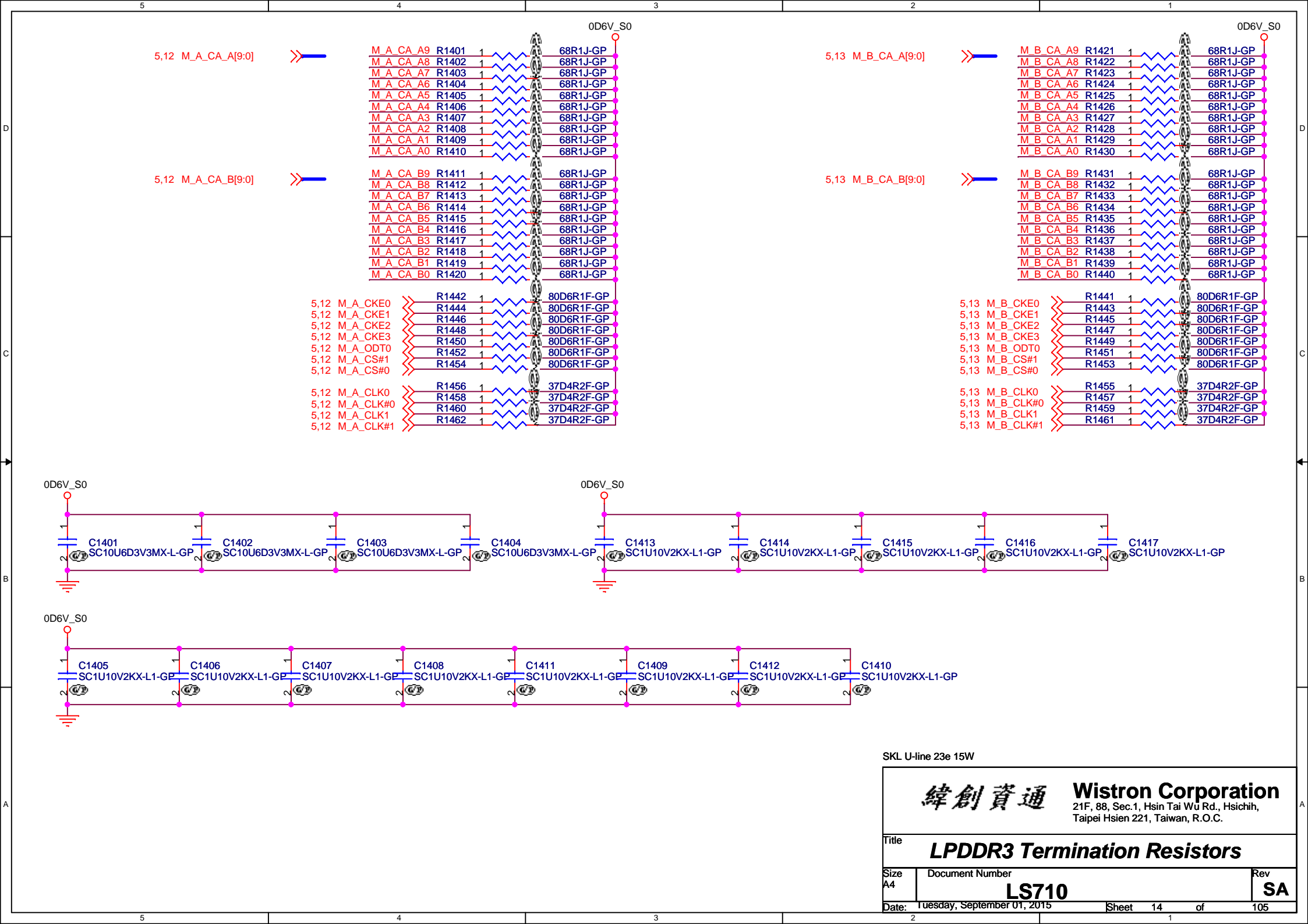
SKL U-line 23e 15W

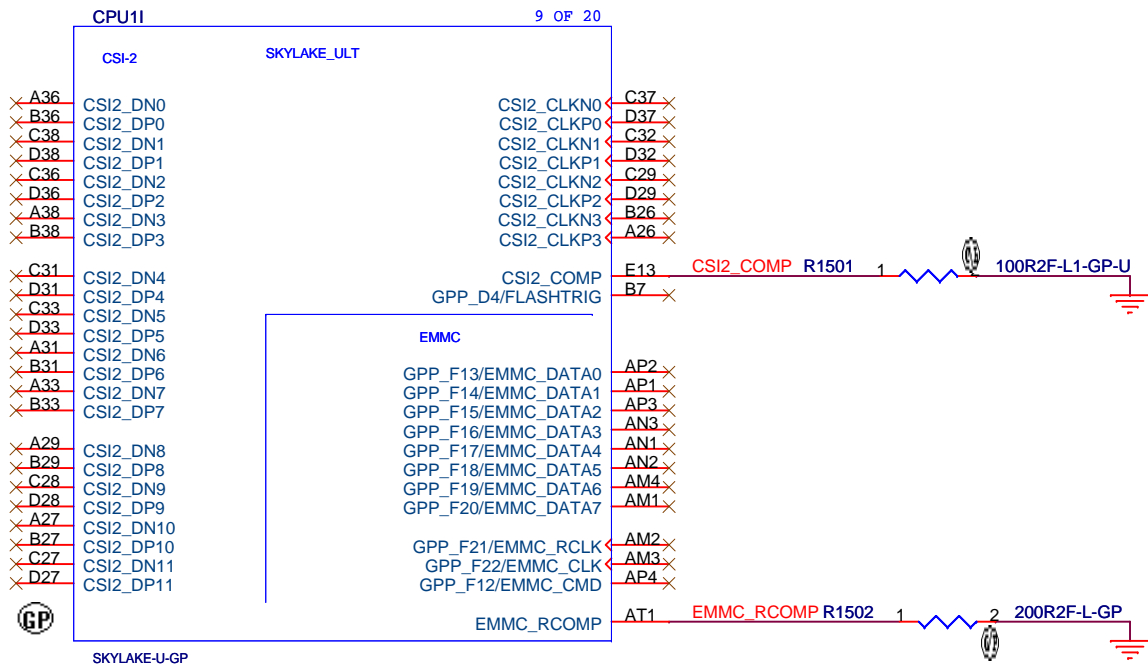
<b>緯創資通</b>		<b>Wistron Corporation</b>	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
<b>CPU (Power CAP2)</b>			
Size	Document Number		Rev
A3	<b>LS710</b>		<b>SA</b>
Date:	Tuesday, September 01, 2015	Sheet 11 of	105











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**Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title

**CPU (CS-2/EMMC)**

Size  
A4

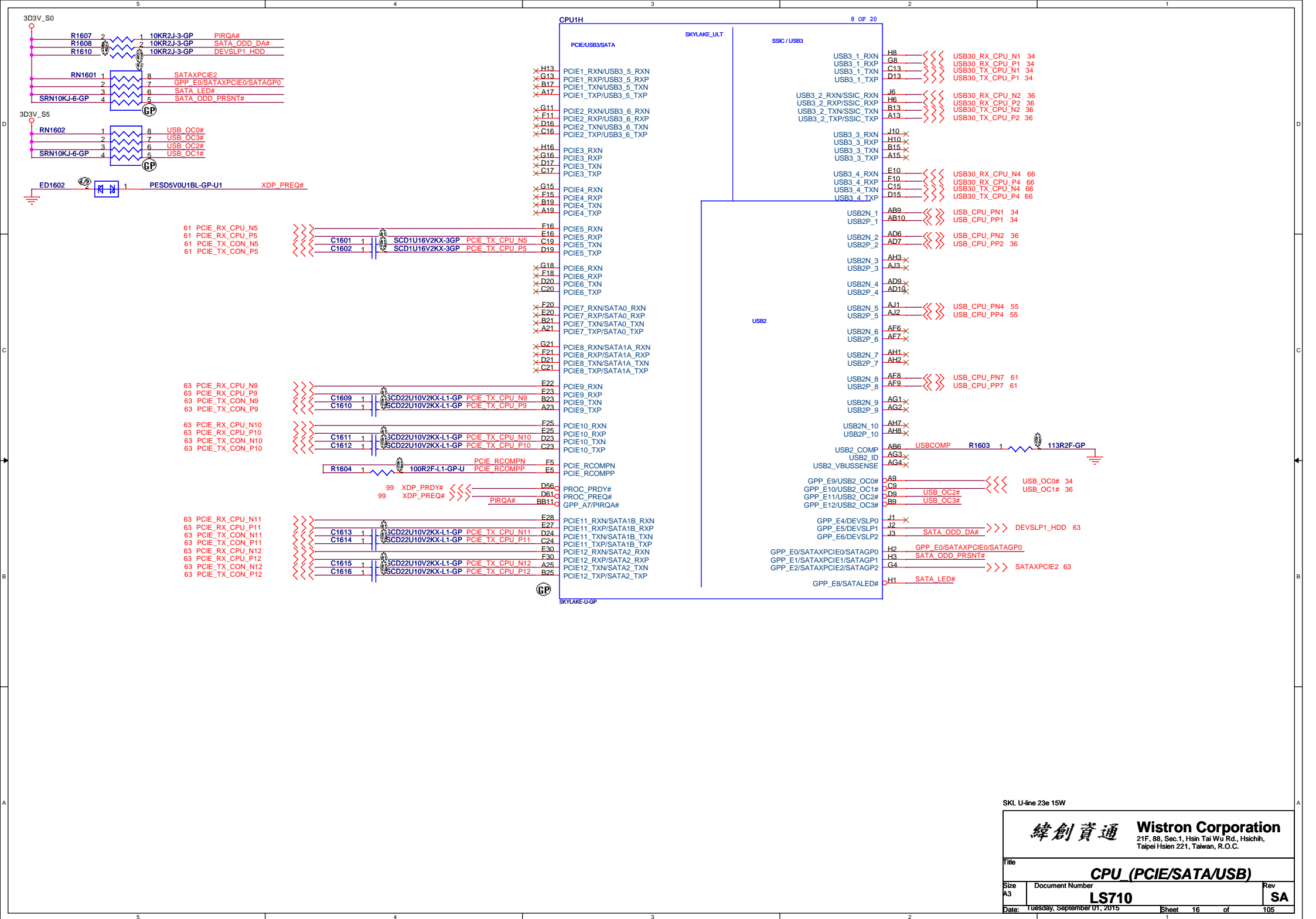
Document Number

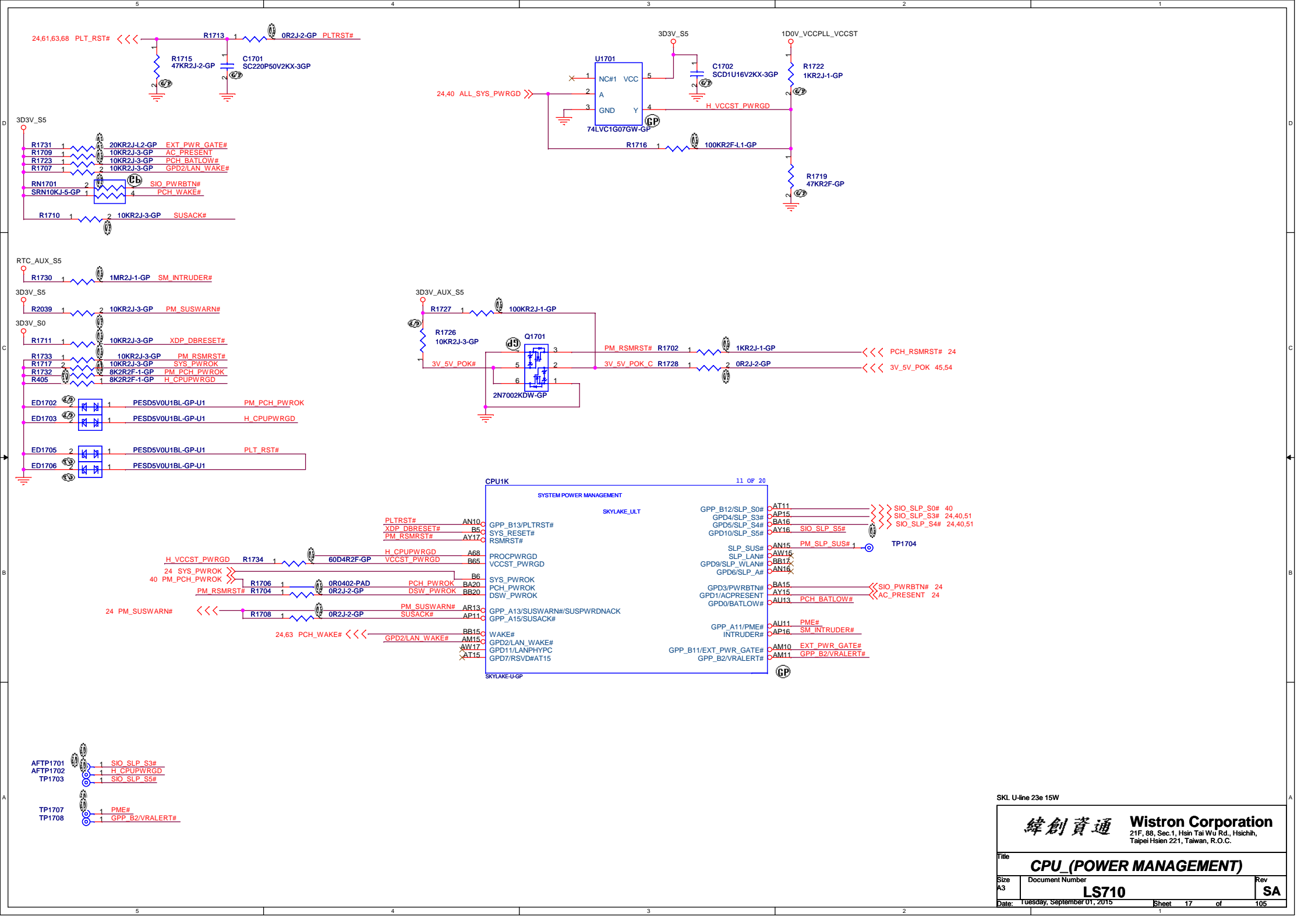
**LS710**

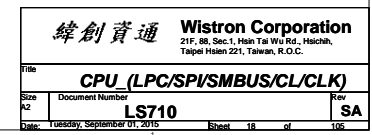
Rev  
**SA**

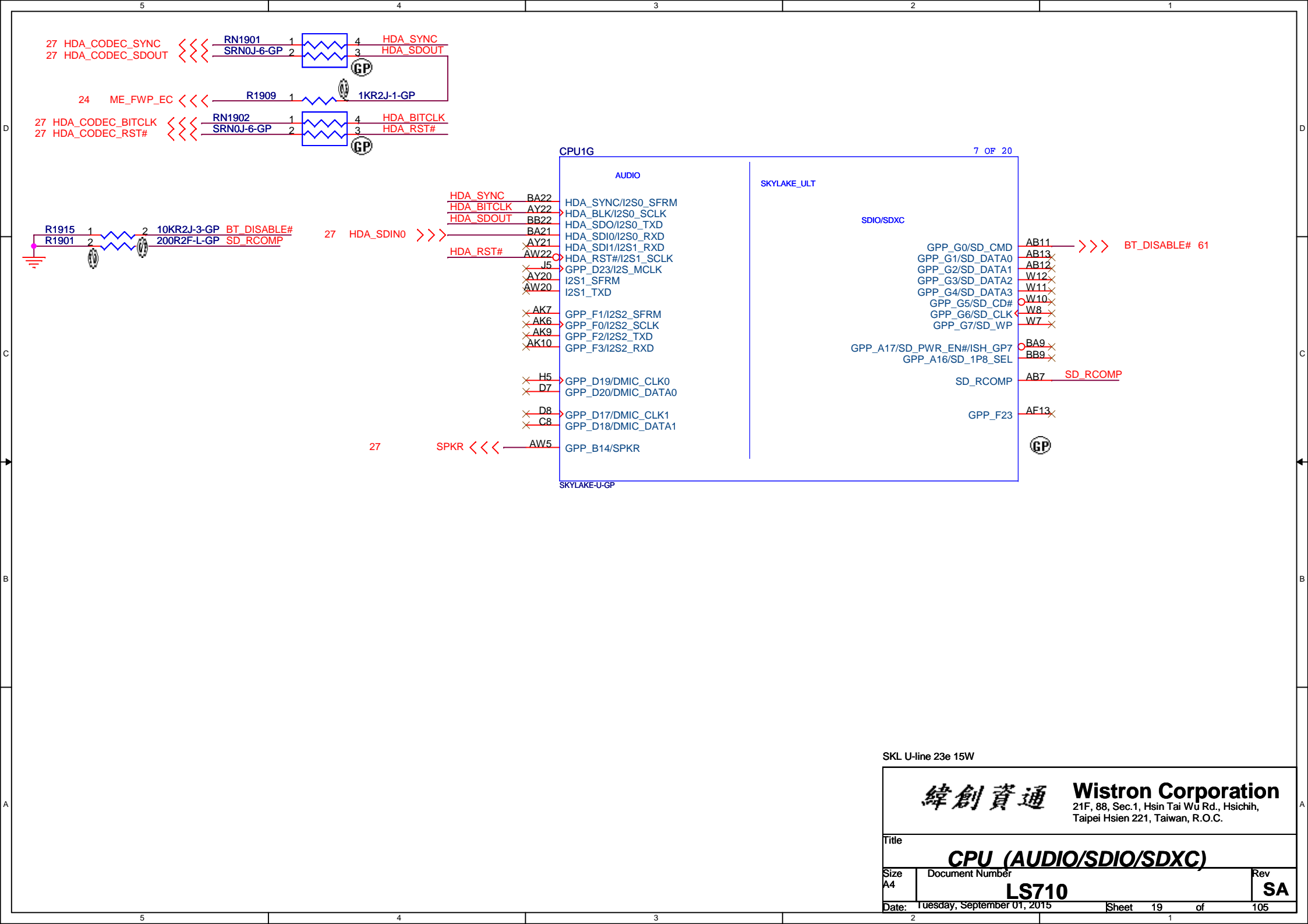
Date: Tuesday, September 01, 2015

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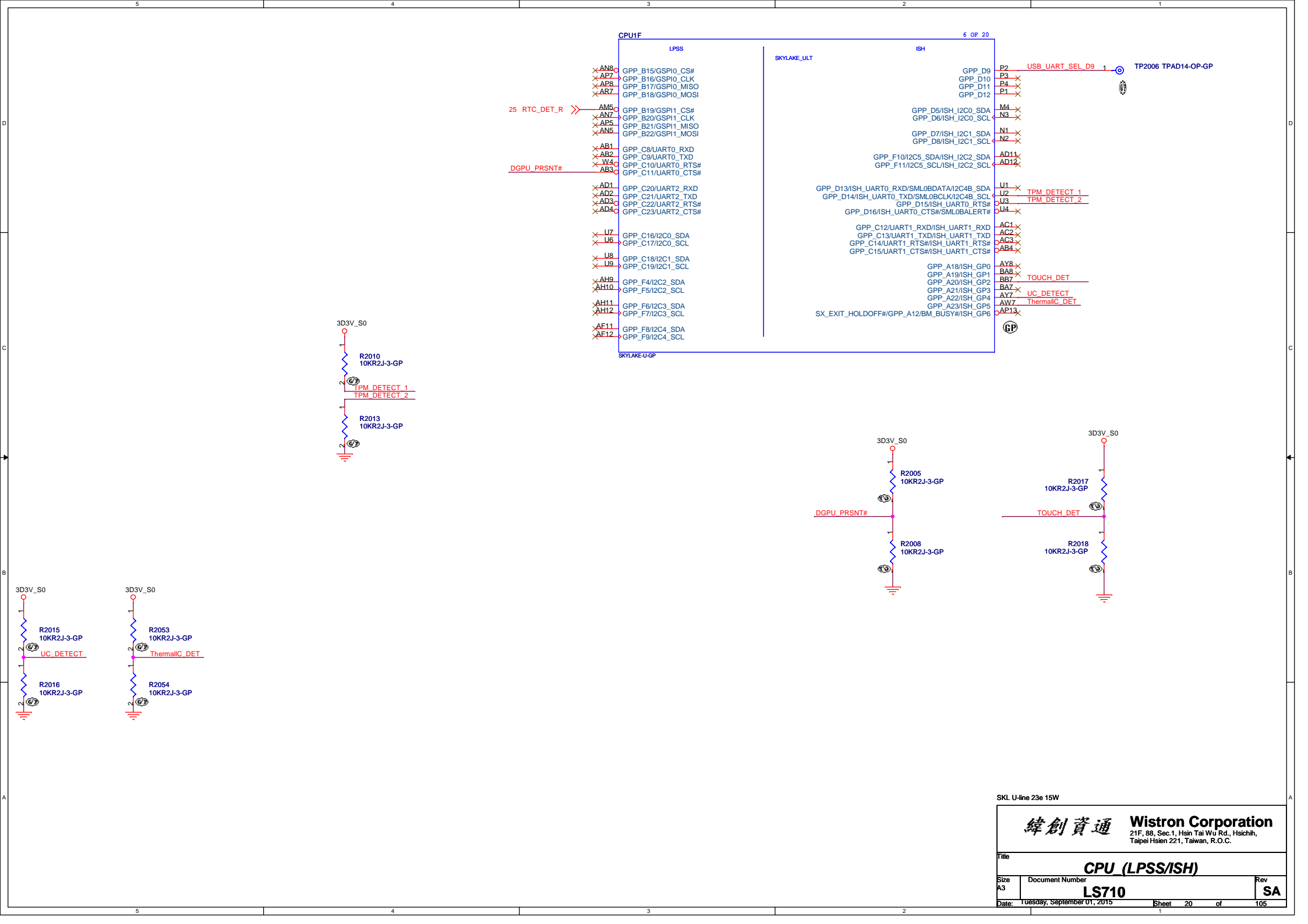




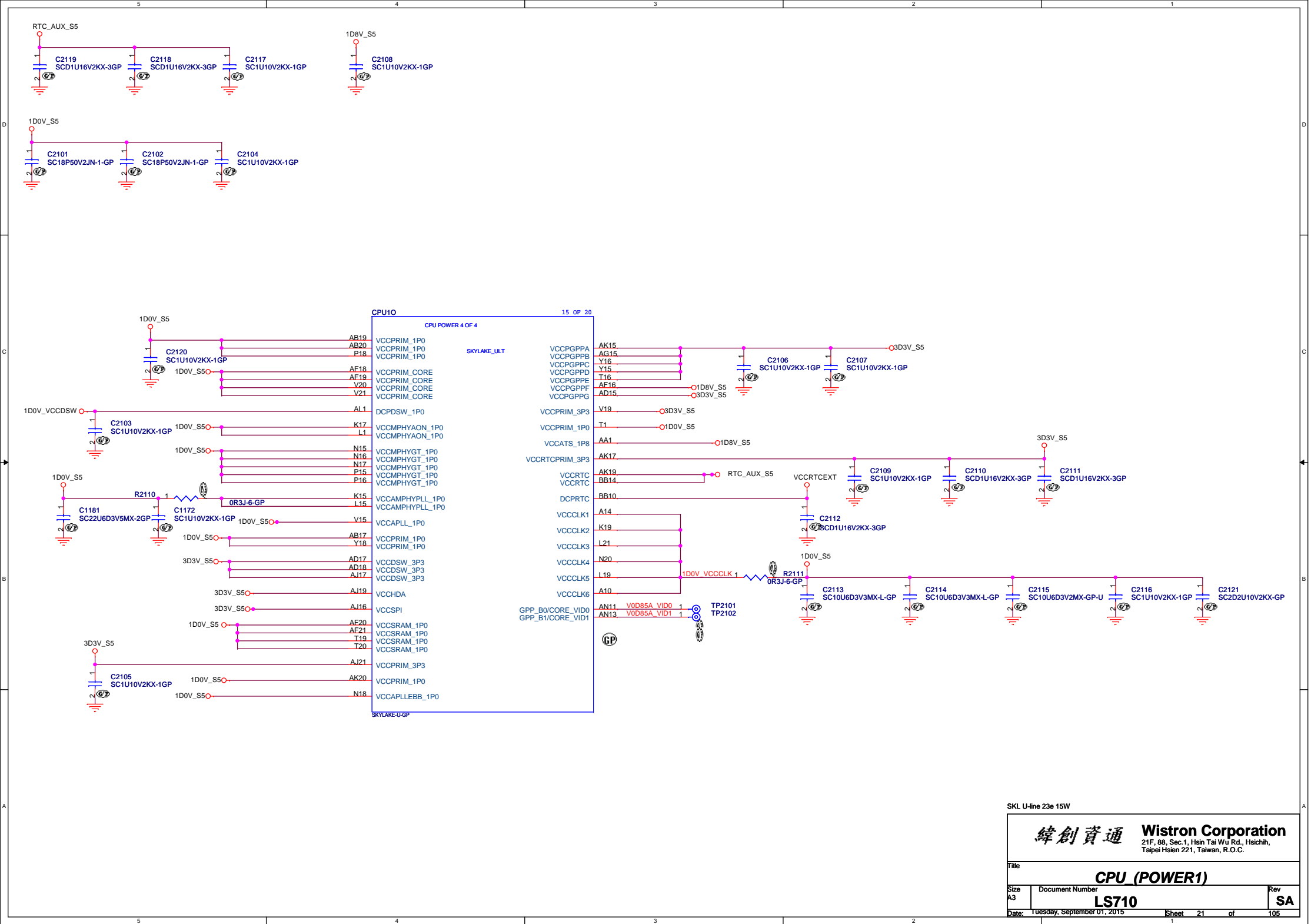
SKL U-line 23e 15W

<div>緯創資通</div> <div>Wistron Corporation</div> <div>21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.</div>		
Title		
CPU (AUDIO/SDIO/SDXC)		
Size A4	Document Number LS710	Rev SA
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CPU1T 20 OF 20

SKYLAKE\_ULT  
SPARE

<del>AW69</del>	RSVD#AW69	RSVD#F6	<del>F6</del>
<del>AW68</del>	RSVD#AW68	RSVD#E3	<del>E3</del>
<del>AU56</del>	RSVD#AU56	RSVD#C11	<del>C11</del>
<del>AW48</del>	RSVD#AW48	RSVD#B11	<del>B11</del>
<del>C7</del>	RSVD#C7	RSVD#A11	<del>A11</del>
<del>U12</del>	RSVD#U12	RSVD#D12	<del>D12</del>
<del>U11</del>	RSVD#U11	RSVD#C12	<del>C12</del>
<del>H11</del>	RSVD#H11	RSVD#F52	<del>F52</del>



SKYLAKE-U-GP

SKL U-line 23e 15W

緯創資通

**Wistron Corporation**  
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Taipei Hsien 221, Taiwan, R.O.C.

Title

**CPU (RSVD)**

Size  
A4

Document Number

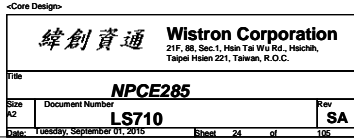
**LS710**

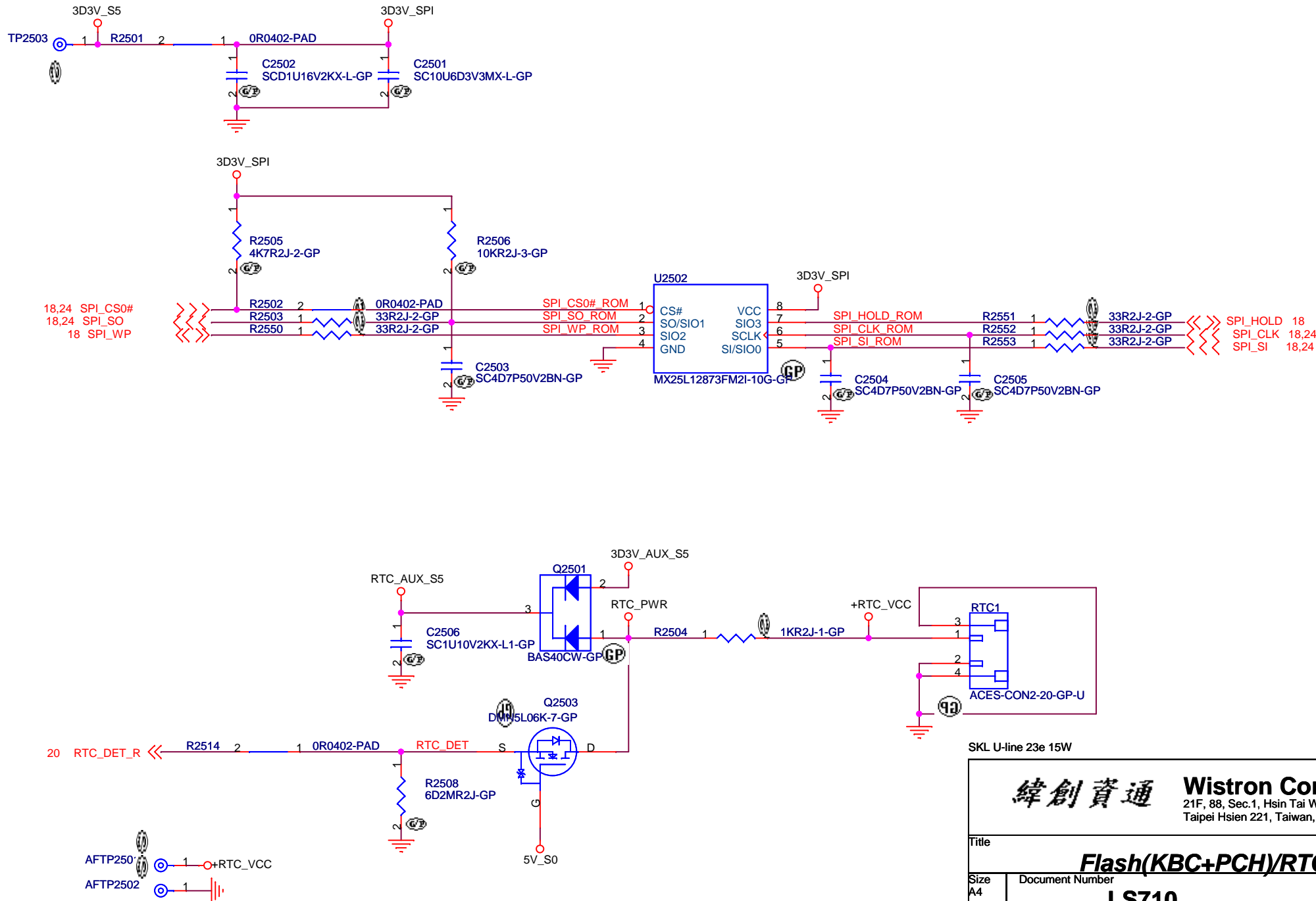
Rev  
**SA**

Date: Tuesday, September 01, 2015

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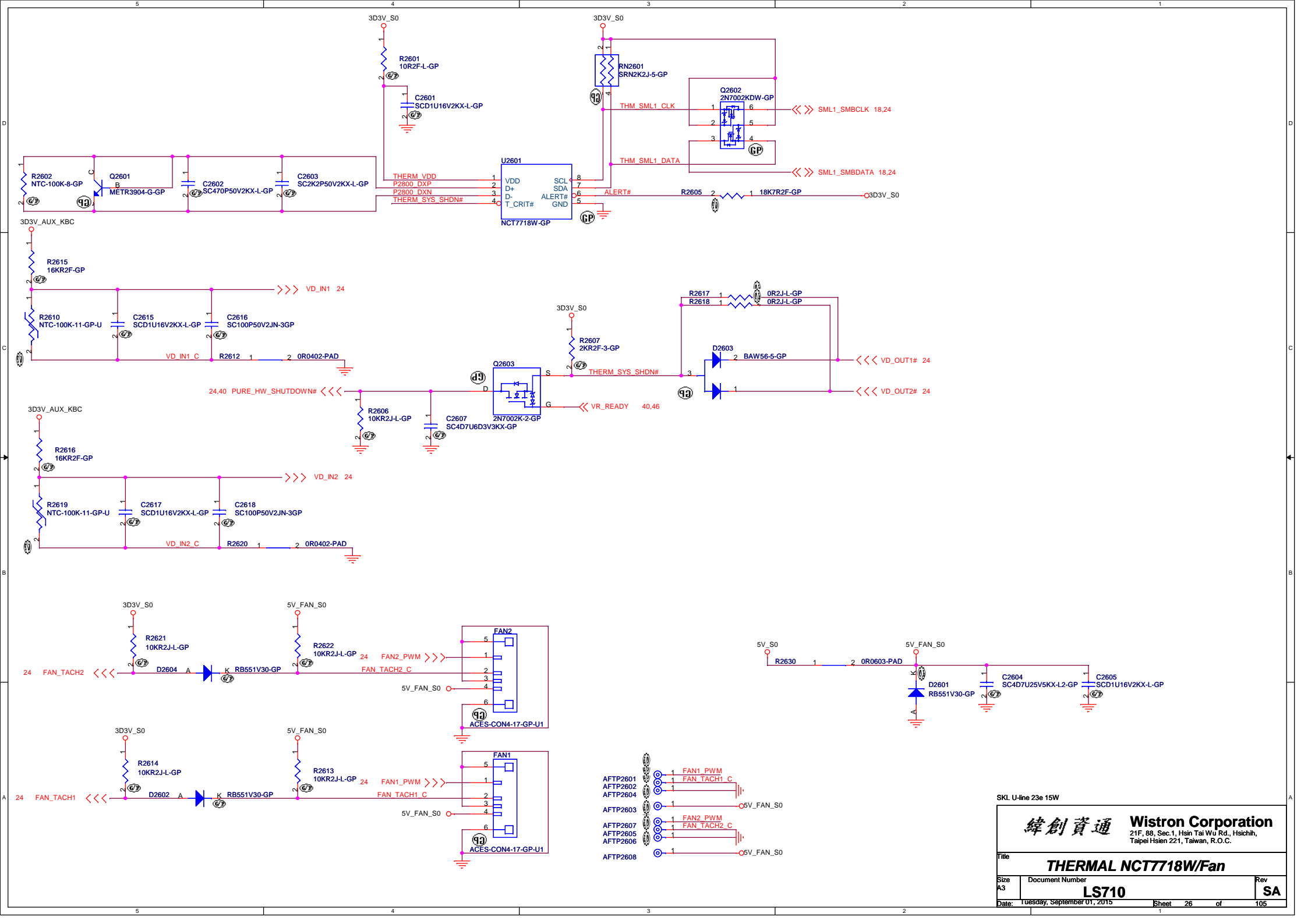




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緯創資通 Wistron Corporation  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

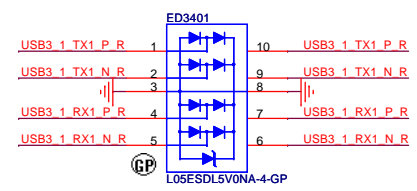
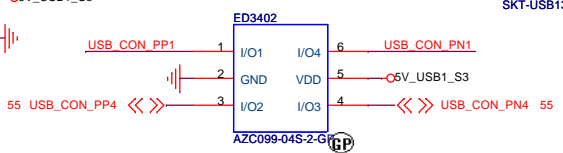
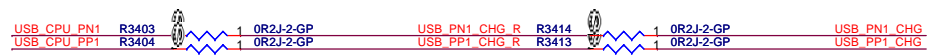
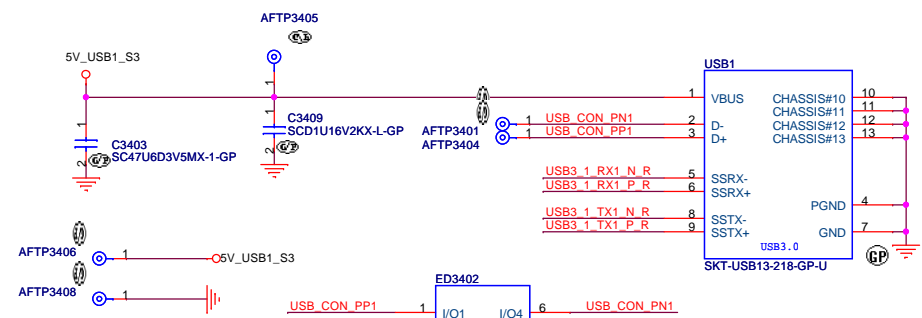
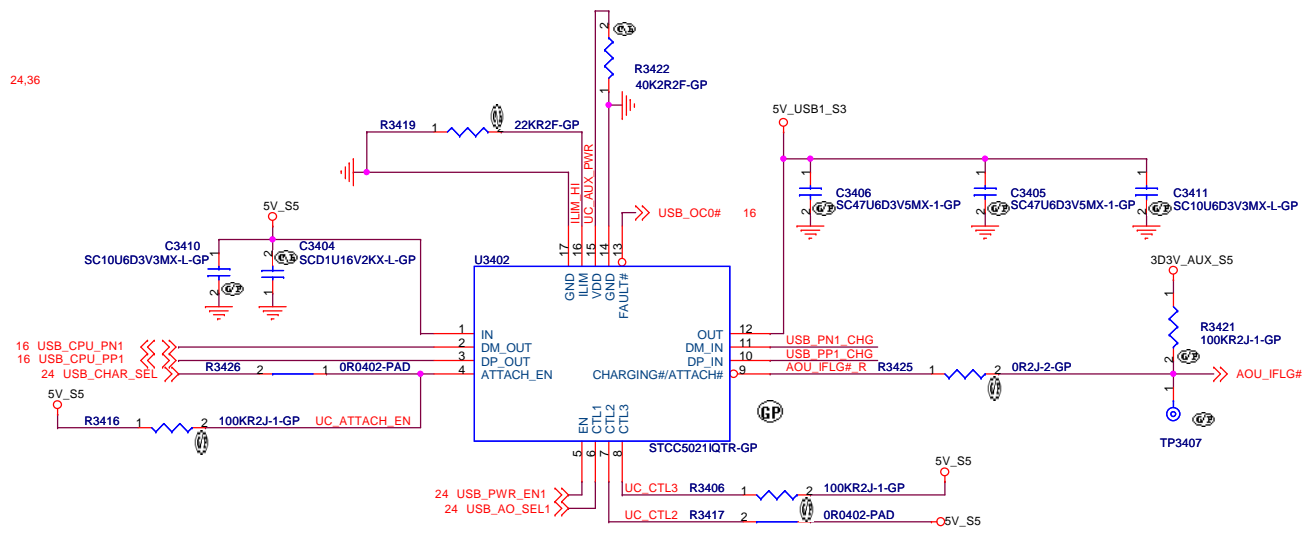
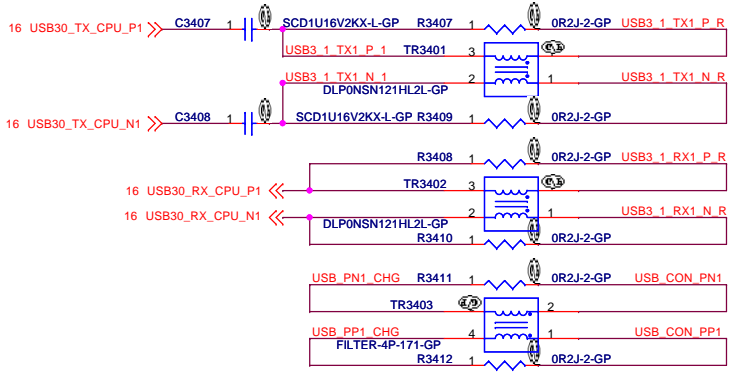
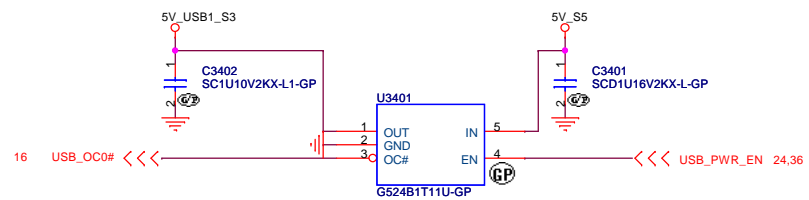
Title		
Flash(KBC+PCH)/RTC		
Size	Document Number	Rev
A4	LS710	SA
Date	Tuesday, September 01, 2015	Sheet 25 of 105







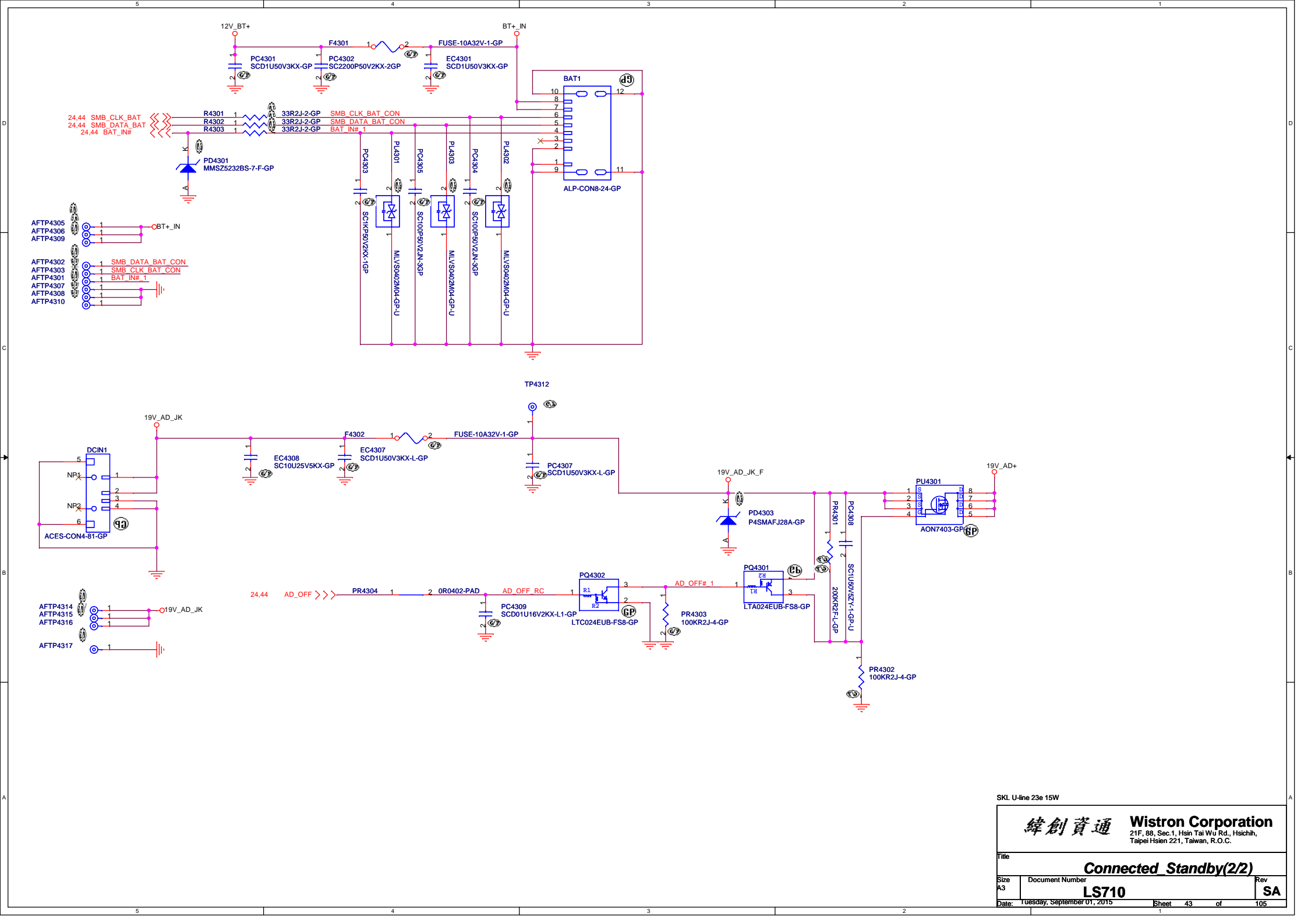




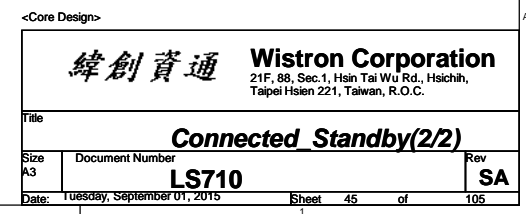
SKL U-line 23e 15W







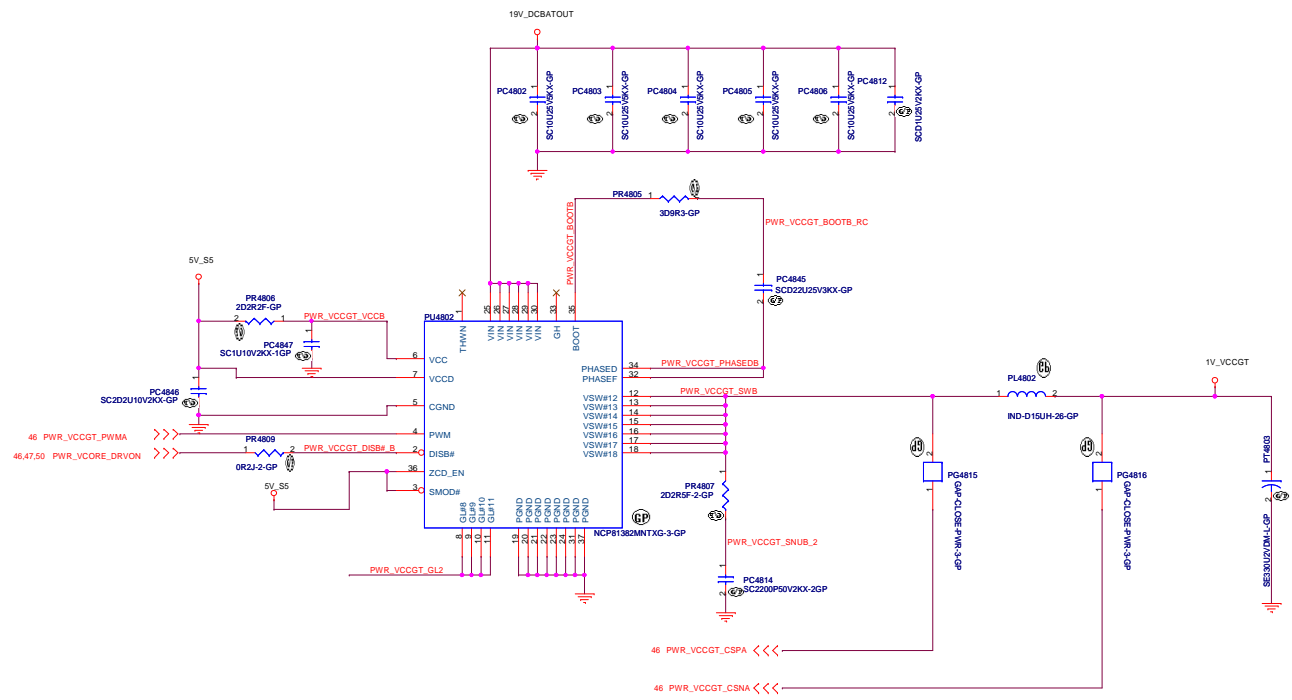
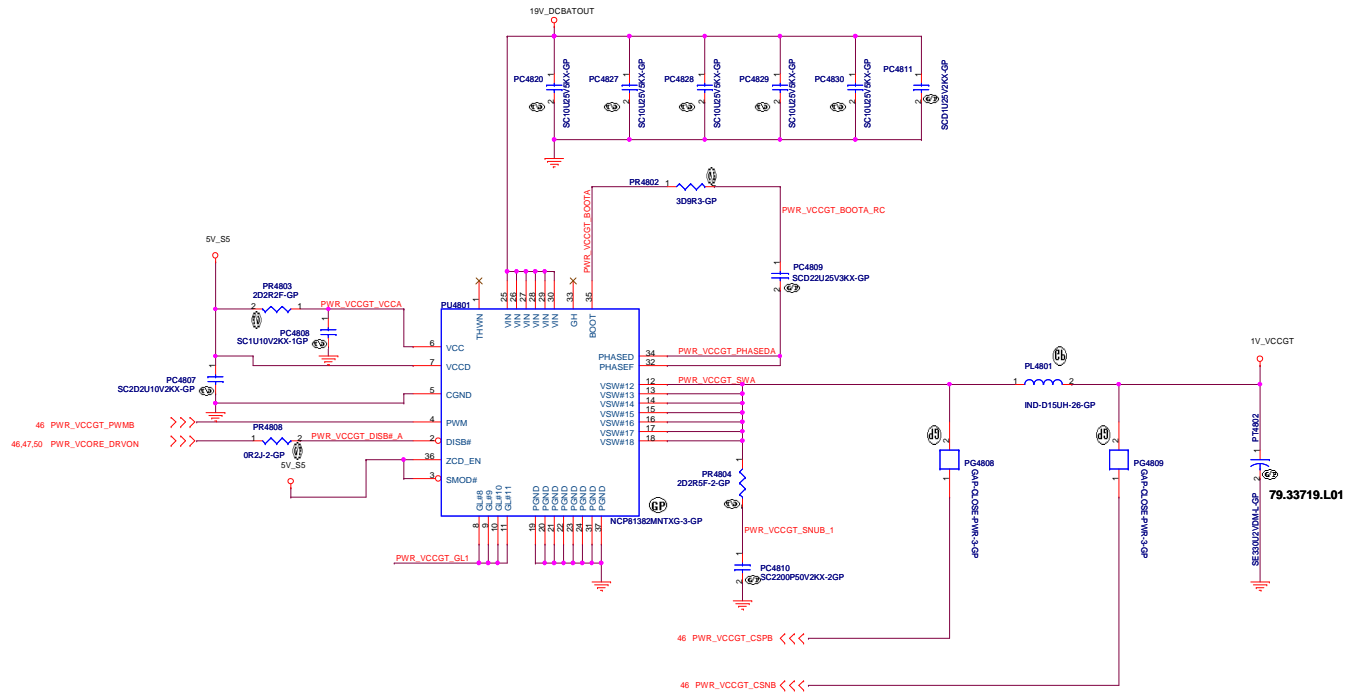


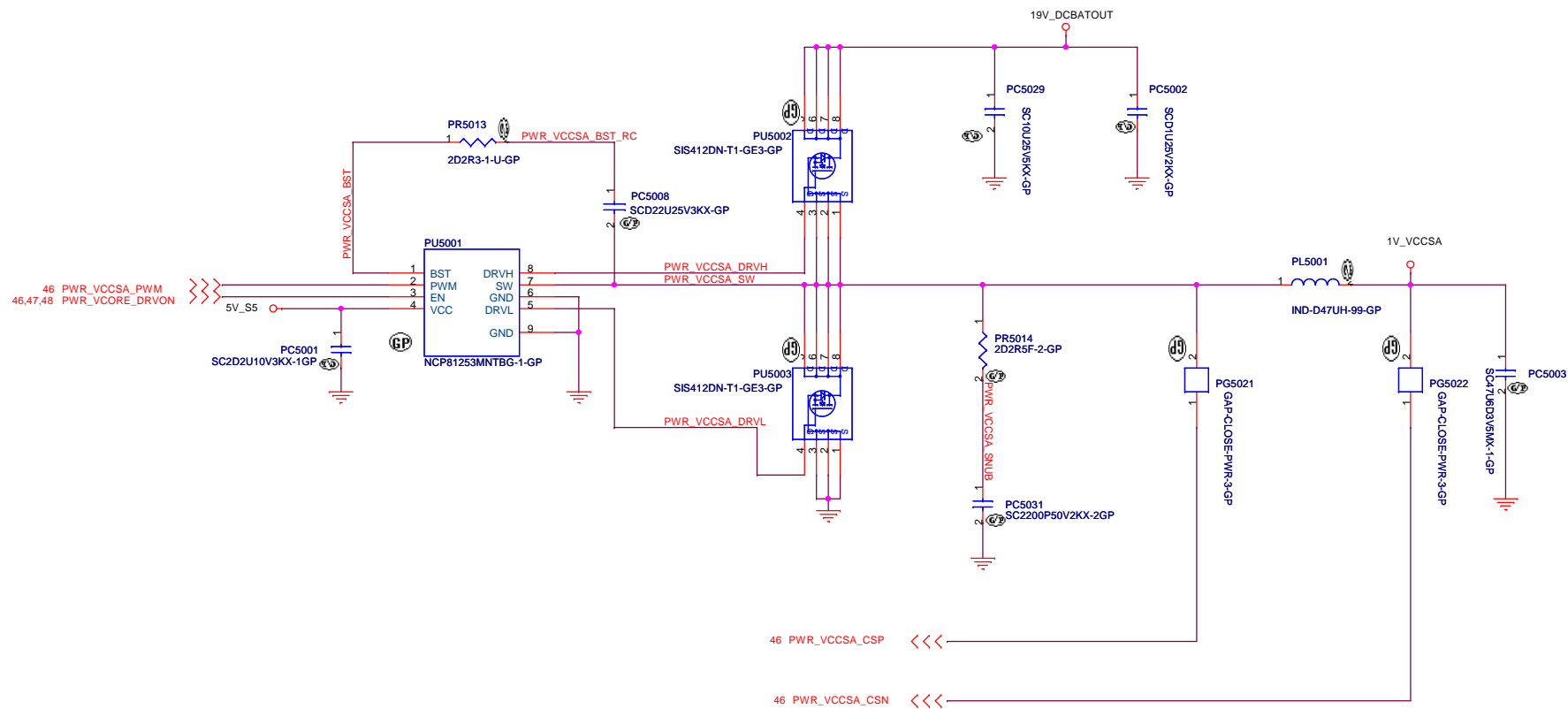






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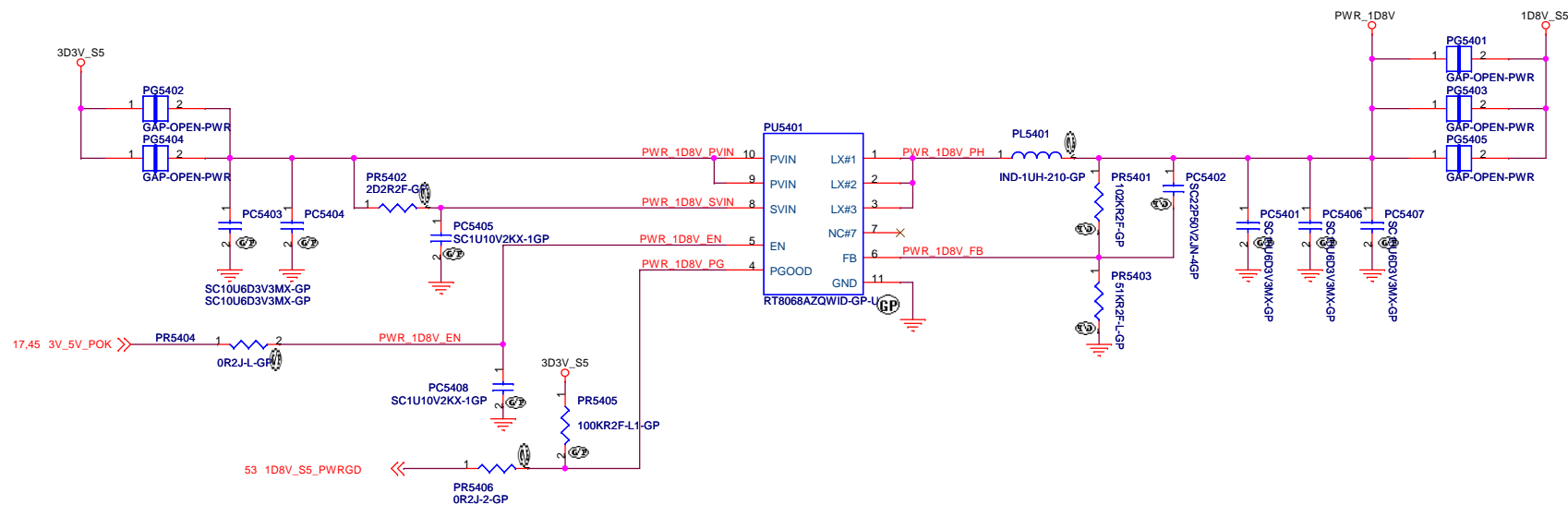




SKL U-line 23e 15W

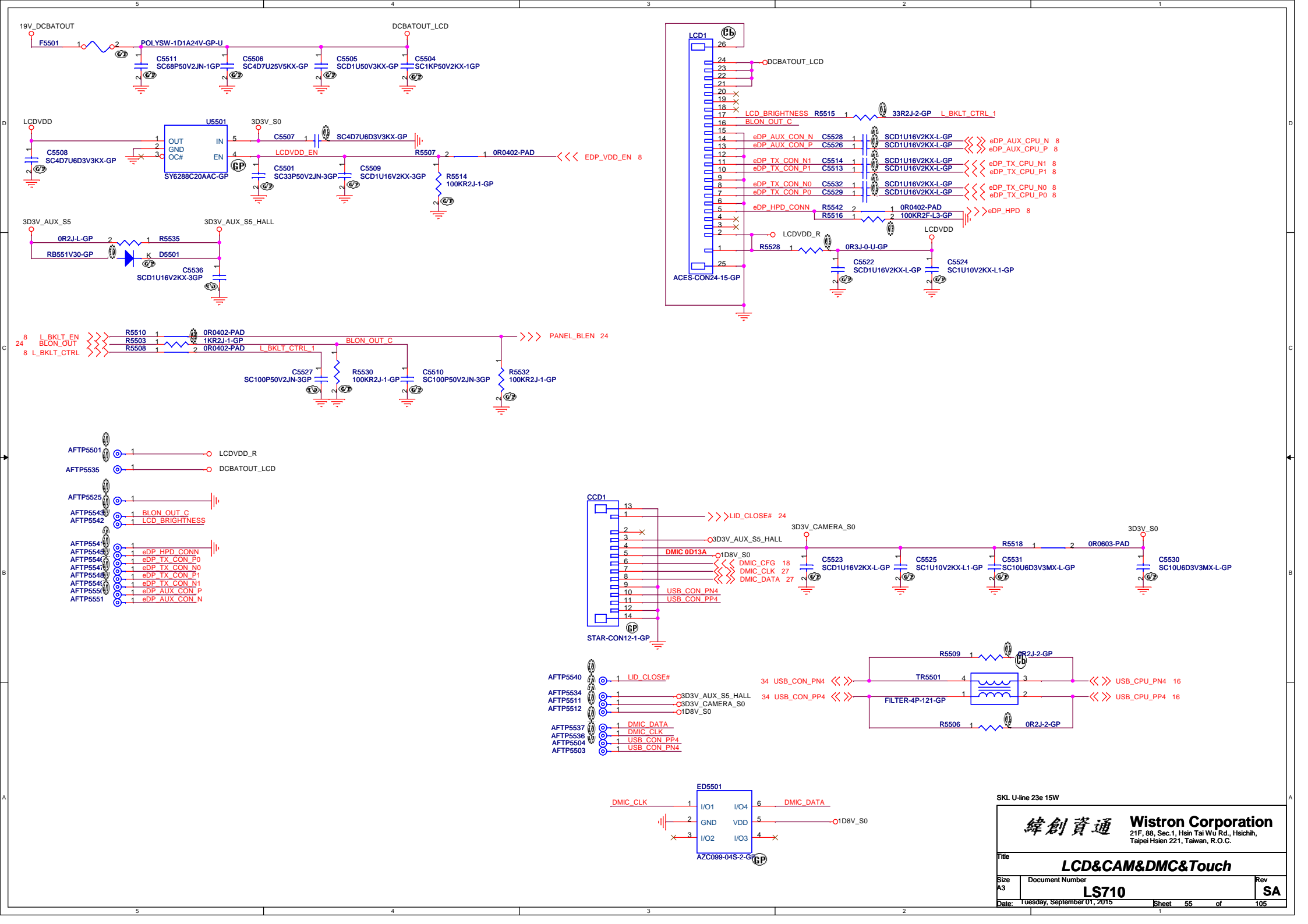


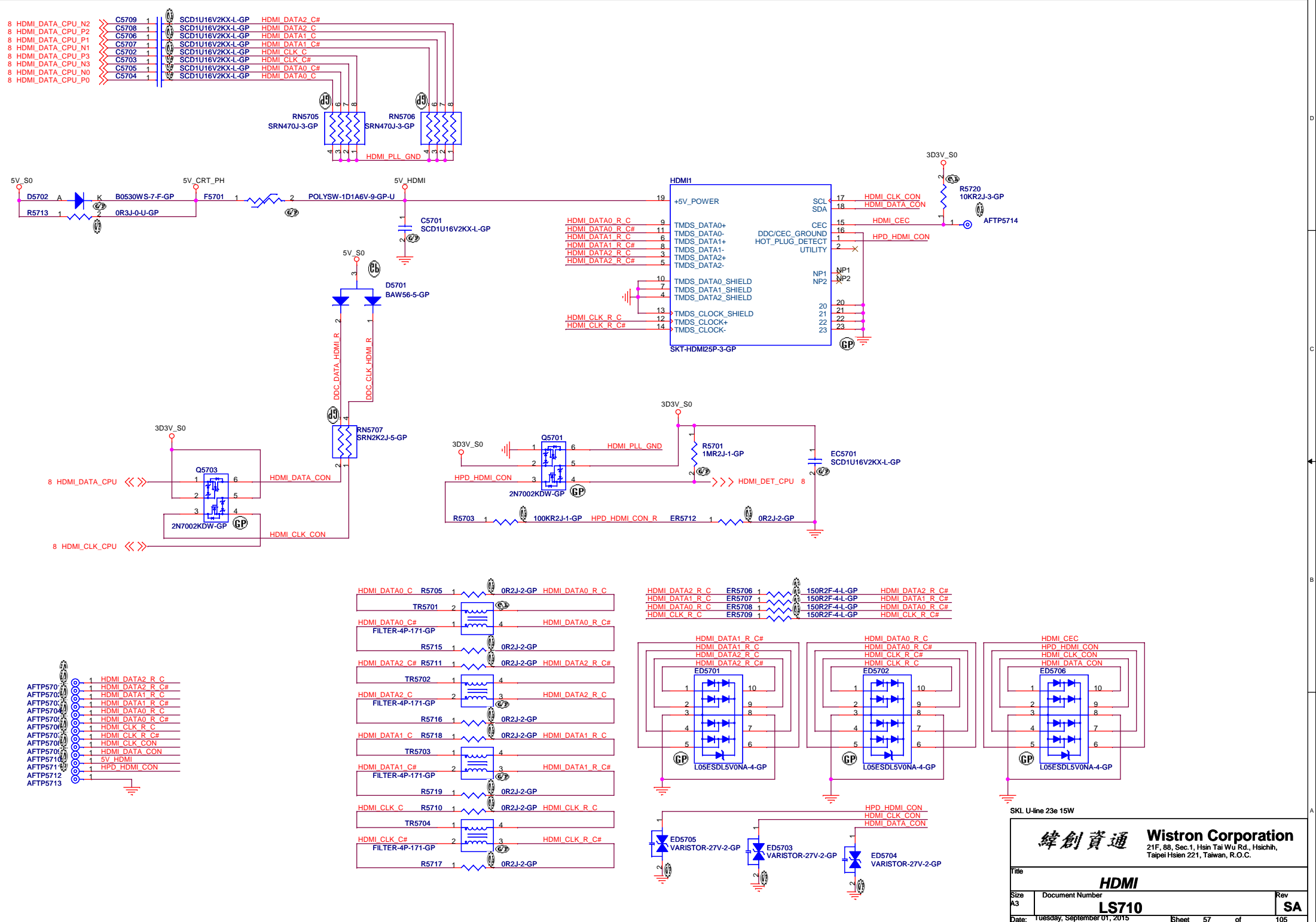




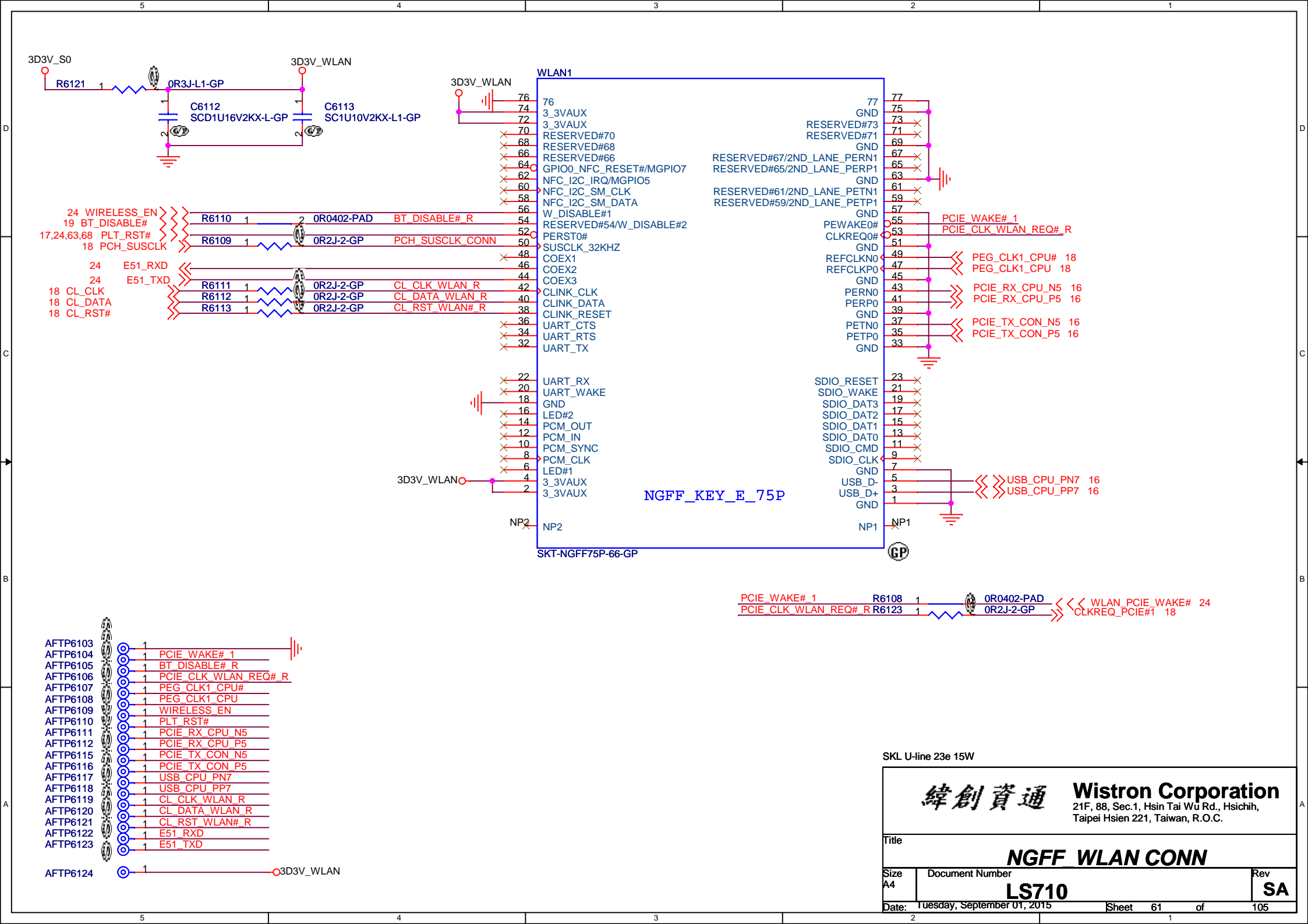
SKL U-line 23e 15W

<b>緯創資通</b> <b>Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title <b>Connected Standby(2/2)</b>	
Size A3	Document Number <b>LS710</b>
Date: Tuesday, September 01, 2015	Rev <b>SA</b>
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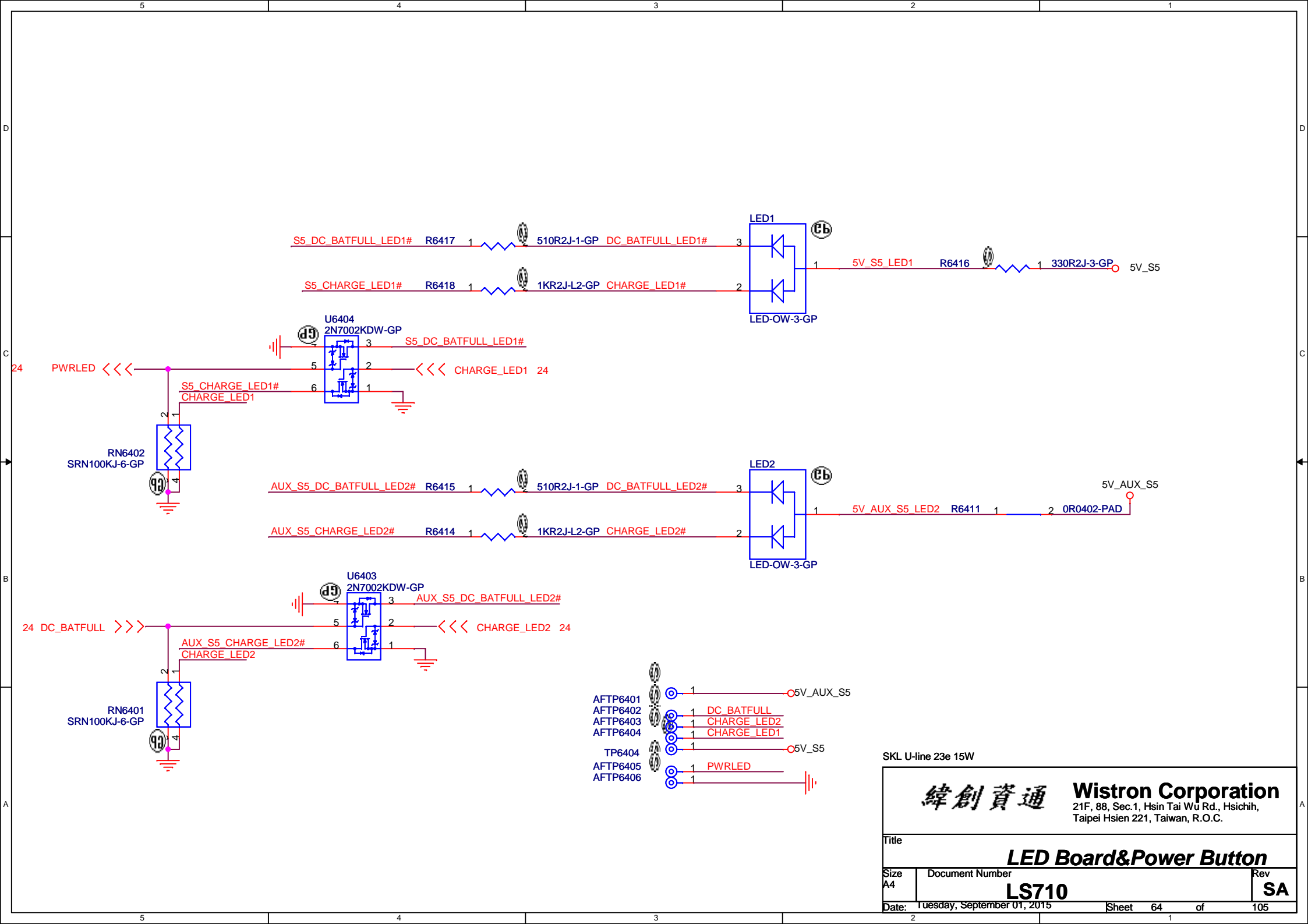








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SKL U-line 23e 15W

緯創資通

**Wistron Corporation**  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title

**LED Board&Power Button**

Size  
A4

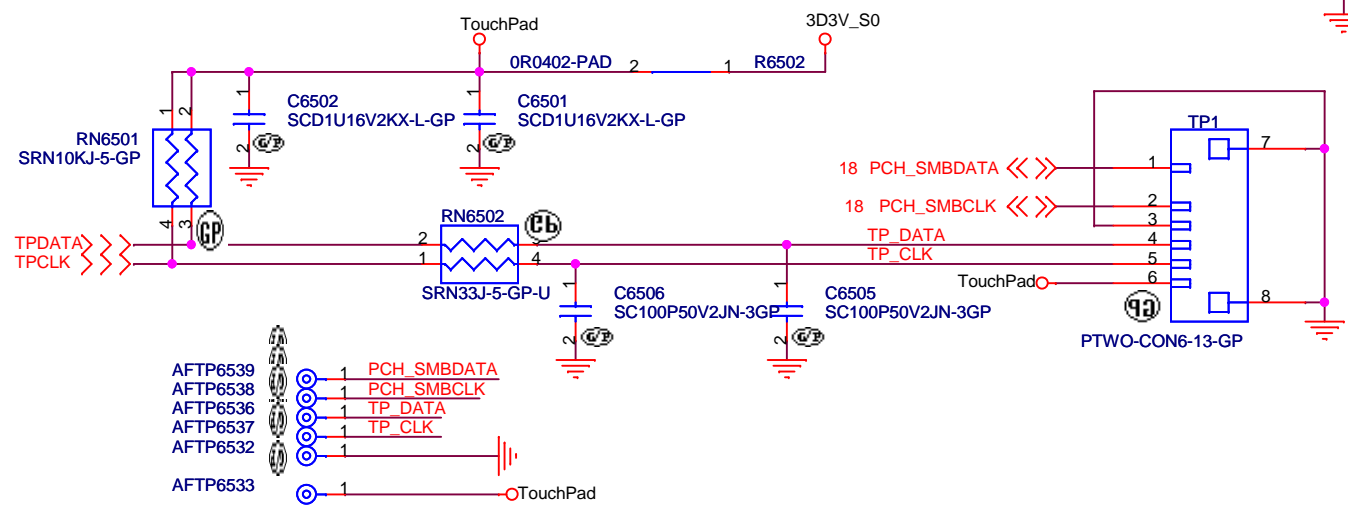
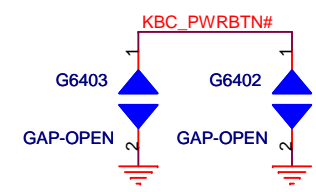
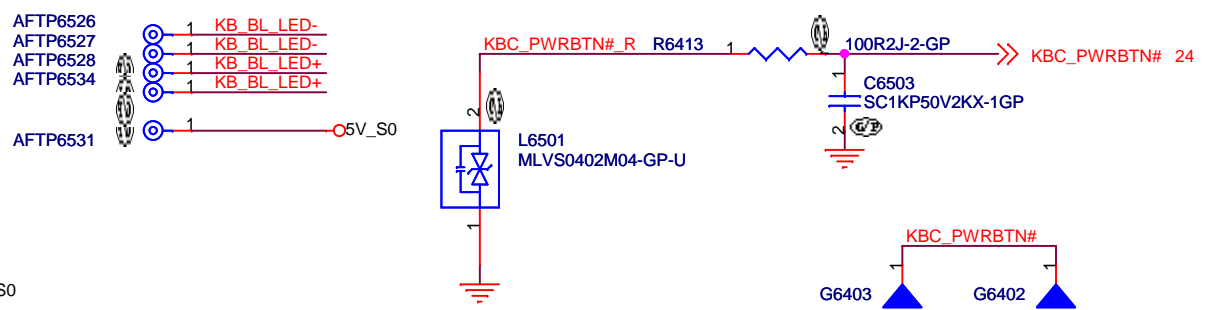
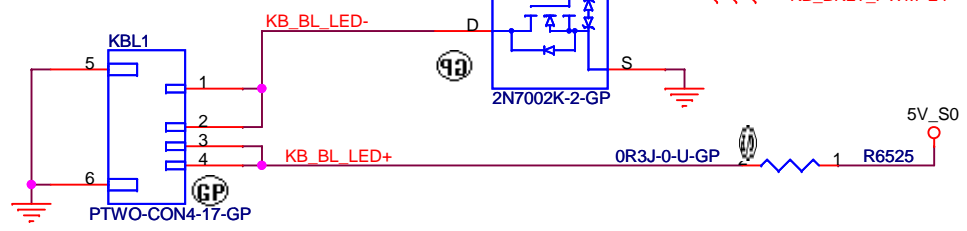
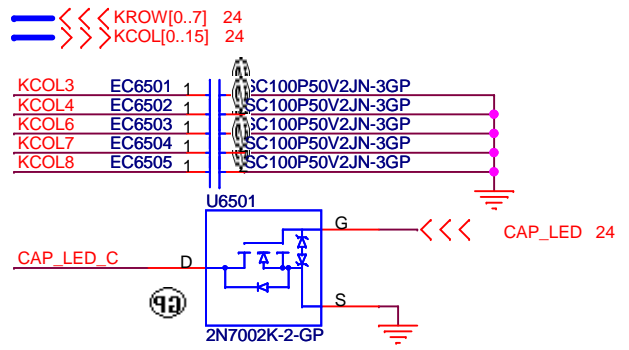
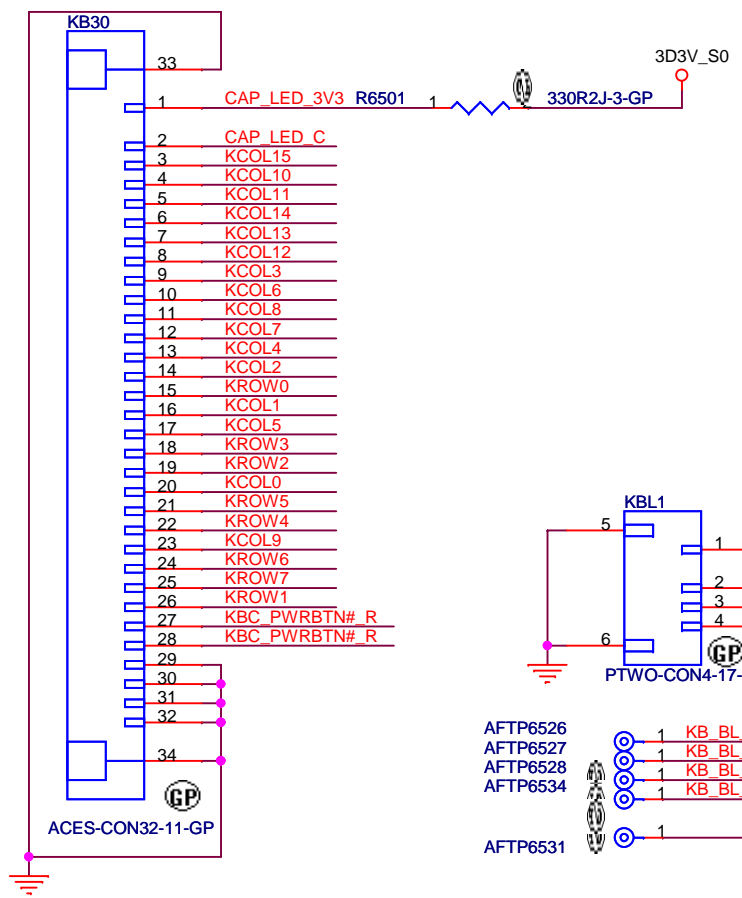
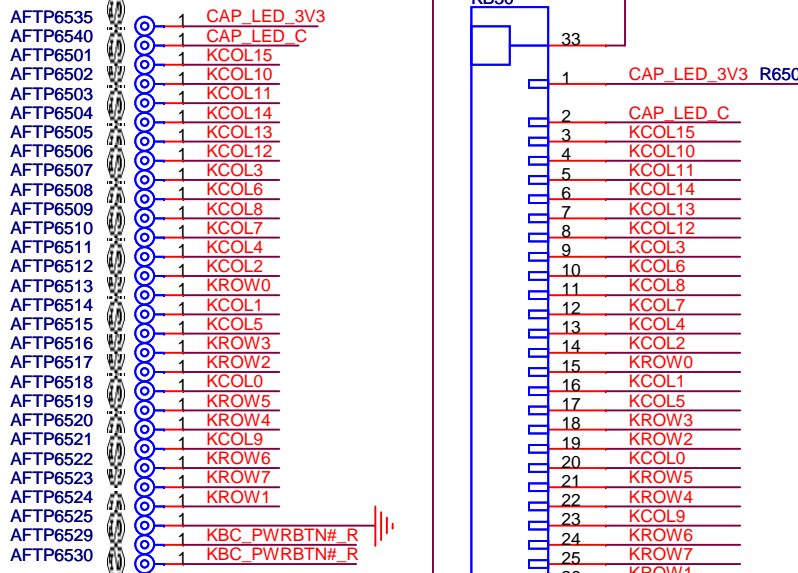
Document Number

**LS710**

Rev  
**SA**

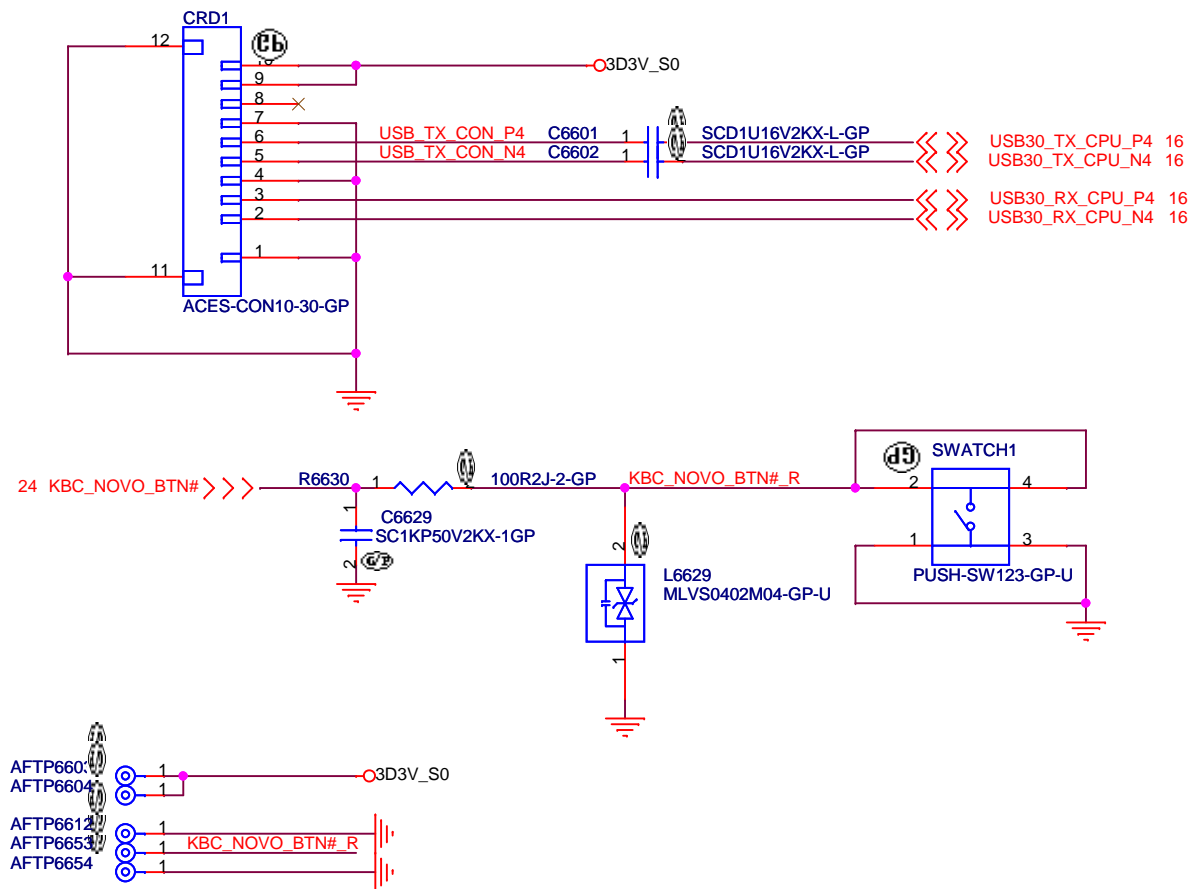
Date: Tuesday, September 01, 2015

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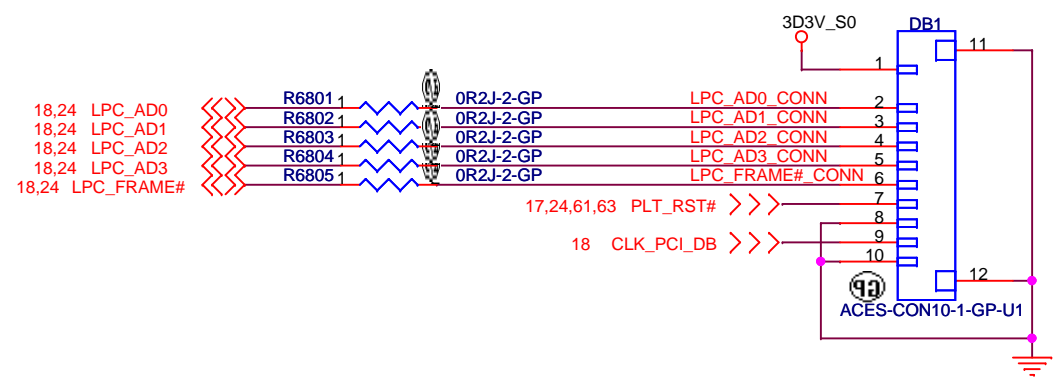
SKL U-line 23e 15W

<b>緯創資通 Wistron Corporation</b> 21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.		
<b>Key Board&amp;Touch Pad</b>		
Title Size A4 Date: Tuesday, September 01, 2015	Document Number <b>LS710</b> Sheet 65 of 105	Rev <b>SA</b>



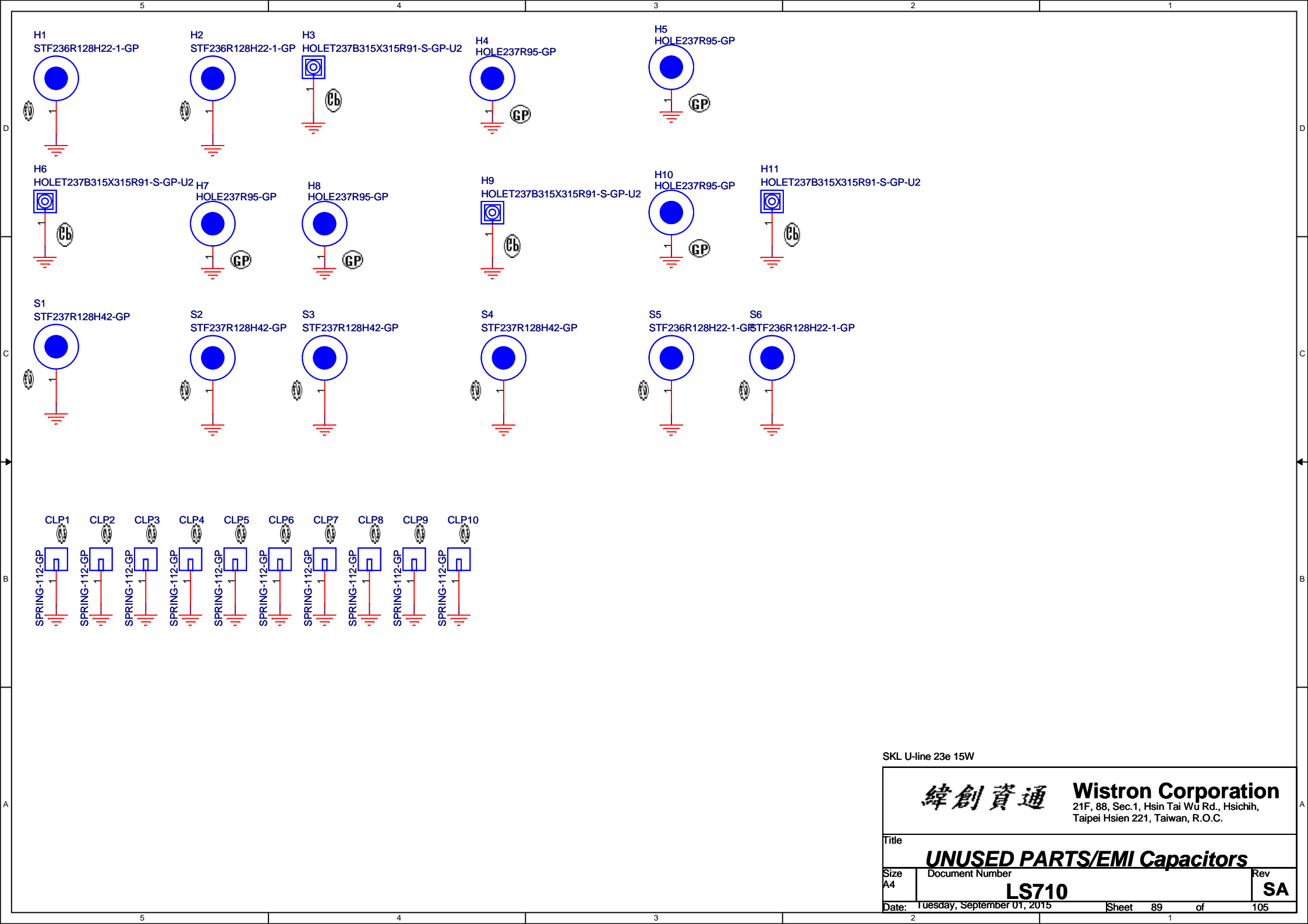
緯創資通 Wistron Corporation  
21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih,  
Taipei Hsien 221, Taiwan, R.O.C.

Title				
IO Board Connector				
Size A4	Document Number			Rev
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<b>緯創資通</b>		<b>Wistron Corporation</b>	
		21F, 88, Sec.1, Hsin Tai Wu Rd., Hsichih, Taipei Hsien 221, Taiwan, R.O.C.	
Title			
<b>Debug connector</b>			
Size	Document Number		Rev
A4	<b>LS710</b>		<b>SA</b>
Date: Tuesday, September 01, 2015		Sheet 68 of 105	



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Title <b>UNUSED PARTS/EMI Capacitors</b>		
Size A4	Document Number <b>LS710</b>	Rev <b>SA</b>
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